

VASAVI COLLEGE OF ENGINEERING (*Autonomous*), IBRAHIMBAGH, HYDERABAD-31

M.E./M.Tech. (CBCS) I-Semester Main Examinations, Jan.-2018

No. 988 /CoE/2017

TIME TABLE

Date: 24-11-2017

Timings: 2.00 pm to 5.00 pm

DATE / DAY	E.C.E.		E.E.E.	MECHANICAL	C.S.E.
	Communication Engineering & Signal Processing	Embedded Systems & VLSI Design	Power Systems & Power Electronics	Advanced Design & Manufacturing	
17.01.2018 Wednesday	Image and Video Processing	Embedded Systems Design	Application of Power Electronics to Power Systems	Mathematical Methods for Engineers	Mobile Computing
19.01.2018 Friday	Advanced Digital Signal Processing	Digital IC Design	Power System Stability	Metal Cutting and Forming	Information Storage & Management
22.01.2018 Monday	Advanced Digital Modulation Techniques	Analog IC Design	Power Electronic Converters	Computer Integrated Design and Manufacturing	Advanced Algorithms
24.01.2018 Wednesday	Data and Computer Communication Networks	Physics of Semiconductor Devices	High Voltage DC Transmission	Finite Element Techniques	Advanced Operating Systems
27.01.2018 Saturday	Adaptive Signal Processing	Advanced Computer Organization	Renewable Energy Sources	Theory of Elasticity and Plasticity	Artificial Intelligence
29.01.2018 Monday	Audio and Speech Signal Processing	VLSI Technology	Power Quality Engineering	Advanced Non-Destructive Evaluation Techniques	Object Oriented Software Engineering
31.01.2018 Wednesday	Finishing School-I (10.00 am to 11.30 am)	Finishing School-I (10.00 am to 11.30 am)	Finishing School-I (10.00 am to 11.30 am)	Finishing School-I (10.00 am to 11.30 am)	Finishing School-I (10.00 am to 11.30 am)

Sd/-

Controller of Examinations

Copy to:

1. The Principal, VCE for information.
 2. The Director, Admissions and Examinations, VCE.
 3. The Head, Department of CSE/ECE/EEE/Mech. Engg./H&SS/Mathematics, VCE.
 4. The Coordinator, Computer Centre, VCE, with a request to upload the timetable in the college website.
 5. The Librarian, VCE for information.
- } with a request to bring it to the notice of all concerned.