Minutes of the Meeting
VASAVI COLLEGE OF ENGINEERING
(Autonomous)
9-5-81, Ibrahimbagh, Hyderabad-500031, Telangana, India
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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Date: 21.5.2019

Minutes of the 9th meeting of Board of Studies in Electronics and Communication Engineering held on 21.5.2019 (Tuesday) between 1.30 P.M. to 5.30 P.M. at the Department of Electronics and Communication Engineering, Vasavi College of Engineering, Ibrahimbagh, Hyderabad.

Members Present:

1.	Dr. E. Sreenivasa Rao	Prof. & Head, Chairman, BoS
2.	Dr. P. Chandrasekhar	OU Nominee, Professor, ECE Dept., OU, Hyderabad
3.	Dr. Mujeebuddin Quadry	Member (PG-Alumnus)
4.	Dr. K. Veera Swamy	Professor
5.	Dr. N.S. Murthy	Professor
6.	Dr. M. Satyam	Professor
7.	Dr. K. Deergha Rao	Professor
8.	Ms. G.R. Padmini	Assoc. Professor
9.	Dr. N.S.S. Reddy	Assoc. Professor
10.	Mrs. A. Sri Lakshmi	Assoc. Professor
11.	Mrs. Vibha D.Kulkarni	Asst. Professor
12.	Mrs. K. Deepthi	Asst. Professor
13.	Mr. N. Abid Ali Khan	Asst. Professor
14.	Mr. R. Goutham	Asst. Professor
15.	Mrs. K.R. Deepthi	Asst. Professor
16.	Mr. B. Uma Mahesh Babu	Asst. Professor
17.	Mr. M. Ramanjaneyulu	Asst. Professor

The following members of Board of Studies could not attend the meeting

1. Dr. L. Anjaneyulu, Professor, Dept. of ECE, NIT, Warangal
2. Dr. Amit Acharya, Electrical Engineering Dept., IIT-Hyderabad
3. Mr. Vemuri Hanuma Sai, Member, Industry Representative
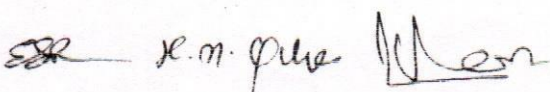4. Prof. P. Venkata Ramana, Professor ECE, VCE.

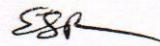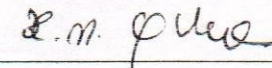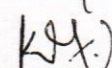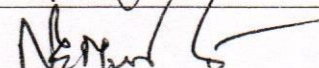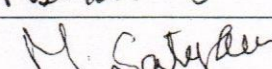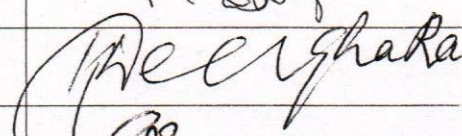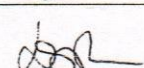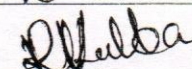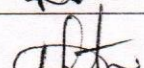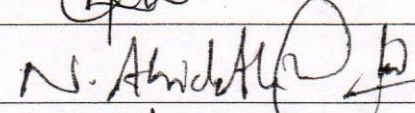Dr. E. Sreenivasa Rao, Chairman BoS presided over the meeting. After he welcomed the members to the meeting, the following items were taken up for discussion and consideration.

1. The minutes of the 8th BoS meeting held on 16-5-2018 were read and confirmed.

2. **Action Taken Report** on the items of 8th BoS meeting held on 16-5-2018 was presented to all the members of BoS. The details are given below.
   - For I-Semester of B.E (E.C.E) program under R-18 regulation, credits are changed from 19.5 to 20 (Engineering Drawing – I credits are changed from 1.5 to 2.0) and II-Semester credits are changed from 20.5 to 21 (Engineering Drawing – II credits are changed from 1.5 to 2.0) as per the directions of the BoG.

3. (a) Vision, Mission and PEO statements of the department are reviewed and suggested that the mission statement is to be in line with the vision statement of the department. Based on the stakeholders feedback, members suggested that ethical and value based education may be incorporated in the mission statement
   (b) Course outcomes are also reviewed and finalized.

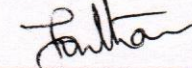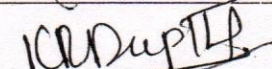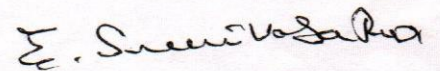4. Proposed course structure, scheme of instruction and examination under **R-19 regulation** for B.E students to be admitted during 2019-20 was presented and resolved the following:
   a. To offer B.E. (E.C.E) program with 162 credits.
   b. To allocate one credit weightage to the theme based project in the VI semester
   c. To offer two credit open elective course in III semester and three credit open elective courses during IV to VI semesters.
   d. Introduction of Mini Projects during IV and V semesters with weightage of one credit.
   e. To offer four streams of professional electives namely Embedded Systems and VLSI Design, Communication Engineering, Signal Processing and Networking during VII and VIII semesters.
   f. Syllabi for I and II semester courses under R-19 regulation was discussed and finalized.

5. Proposed scheme of instruction and examination under **R-18 regulation** for B.E students was presented and resolved the following:
   a. To allocate one credit to the theme based project in the VI semester
   b. To offer two credit open elective course in III semester and three credit open electives courses during IV to VI semesters.
   c. Introduction of Mini Projects during IV semester with weightage of one credit.
   d. To offer four streams of professional electives namely Embedded Systems and VLSI Design, Communication Engineering, Signal Processing and Networking during VII and VIII semesters.
   e. Syllabi for III and IV semester courses under R-18 regulation was discussed and finalized.

6. Proposed scheme of instruction and examination under **R-17 regulation** for B.E students was presented and resolved the following:
   a. To offer four streams of professional electives namely Embedded Systems and VLSI Design, Communication Engineering, Signal Processing and Networking during VII and VIII semesters.

b. Syllabi for V and VI semester courses under R-17 regulation was discussed and finalized.

7. Proposed scheme of instruction and examination under **R-16 regulation** for B.E students was presented and resolved the following:
   a. To offer four streams of professional electives namely Embedded Systems and VLSI Design, Communication Engineering, Signal Processing and Networking during VII and VIII semesters.

   Following New Professional Electives are introduced:

   IoT Architecture and Protocols, Advanced Embedded Systems, Fields Programmable Gate Arrays (FPGA) Architectures, Low Power VLSI Design, Image and Video Processing using Machine Learning, Network Management, Voice and Data Networks, Optical Networks, Software Defined and Cognitive Radio Networks.
   b. Syllabi for VII and VIII semester courses under R-16 regulation was discussed and finalized.

8. Proposed course structure, scheme of instruction and examination under **R-19 regulation** for M.E students in specialization ES&VLSID and CE&SP to be admitted during 2019-20 was presented and resolved the following:
   a. To offer five professional elective courses instead of six. Two professional electives will be offered during I - semester and two during II - semester and one during III semester.
   b. To introduce Skill Development Course - I (Communication Skills with one credit weightage and Technical Skills with two credits weightage) during I – semester and Skill Development Course - II (Soft Skills with one credit weightage and Technical Skills with two credits weightage) during II – semester.
   c. To offer only one Seminar during I-semester instead of one seminar each during I and II semesters.
   d. BoS University nominee, suggested to offer VLSI Physical Design course as core course instead of Professional Elective during II – semester and Physics of Semiconductor Devices course as the professional elective instead of core course during I – semester for M.E ES&VLSID program.
   e. To offer M.E. ES&VLSID and CE&SP programs with total 68 credits.
   f. Syllabi for I and II semester courses under R-19 regulation was discussed and finalized.

9. Proposed scheme of instruction and examination under **R-18 regulation** for ME students was presented and resolved the following:
   a. Syllabi for III and IV semester courses under R-18 regulation was discussed and finalized.

10. It has been resolved that chairman will be signing all the syllabi of UG & PG programs, discussed in the present meeting, indicating the approval of BoS.

11. The meeting concluded with vote of thanks to the Chair.

S.No.	BoS Members	Signature
1.	Dr. E. Sreenivasa Rao	
2.	Dr. P. Chandrasekhar	
3.	Dr. Mujeebuddin Quadry	
4.	Dr. K. Veera Swamy	
5.	Dr. N.S. Murthy	
6.	Dr. M. Satyam	
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15.	Mrs. K.R. Deepthi	
16.	Mr. B. Uma Mahesh Babu	
17.	Mr. M. Ramanjaneyulu	

(Dr. E. SREENIVASA RAO)
Chairman, BoS-E.C.E