

VASAVI COLLEGE OF ENGINEERING (*Autonomous*), IBRAHIMBAGH, HYDERABAD-31
M.E. (CBCS) I-Semester Make-up Examinations, February/March.-2019

No.21/CoE/2019

TIME TABLE

Date: 11-02-2019

Timings: 10.00 am to 1.00 pm

DATE / DAY	E.C.E.		E.E.E.	MECHANICAL
	Communication Engineering & Signal Processing	Embedded Systems & VLSI Design	Power Systems & Power Electronics	Advanced Design & Manufacturing
25.02.2019 Monday	Audio and Speech Signal Processing	Advanced Computer Organization	Advanced Computer Methods in Power Systems	Metal Cutting and Forming
27.02.2019 Wednesday	Advanced Digital Signal Processing	Digital IC Design	Power Quality Engineering	Flexible Manufacturing Systems
01.03.2019 Friday		Analog IC Design	Power Electronic Converters	Mathematical Methods for Engineers
05.03.2019 Tuesday			High Voltage DC Transmission	Computer Integrated Design and Manufacturing
07.03.2019 Thursday			Application of Power Electronics to Power Systems	Finite Element Techniques
11.03.2019 Monday			Power System Stability	
13.03.2019 Wednesday			English for Research Paper Writing	

Note: Clash/Omission(s) if any, may be immediately brought to the notice of the undersigned.

Copy to:

1. The Principal, for kind information.
2. The Director, Admissions and Examinations.
3. The Head, Department of ECE/EEE/Mech. Engg./H&SS/Mathematics.
4. The Coordinator, Computer Centre, with a request to upload the timetable in the college website.
5. The Librarian, for information.
6. Office file.

} with a request to bring it to
} the notice of all concerned.

Sd/-
Controller of Examinations