VASAVI COLLEGE OF ENGINEERING (Autonomous), IBRAHIMBAGH, HYDERABAD-31

M.E./M.Tech. (CBCS) II-Semester Main Examinations, July-2017

No. 786/M.E./M.Tech./Exams/TT/2017

TIME TABLE

Timings: 10.00 am to 1.00 pm

DATE / DAY	E.C.E.		E.E.E.	MECH. ENGG.	
	Communication Engineering & Signal Processing	Embedded Systems & VLSI Design	Power Systems & Power Electronics	Advanced Design & Manufacturing	C.S.E.
12-07-2017 Wednesday	Coding Theory and Techniques	Mixed Signal IC Design	Distribution System Planning and Automation	Design for Manufacture and Assembly	Distributed Computing
14-07-2017 Friday	Wireless Communications and Networking	Embedded Real Time Operating Systems	Power Electronics Controlled Electric Drives	Metallurgy of Metal Casting and Welding Processes	Advanced Databases
17-07-2017 Monday	Image and Video Processing	Low Power VLSI Design	Advanced Computer Methods in Power Systems	Computer Aided Mechanical Design and Analysis	Image Processing
19-07-2017 Wednesday	MIMO Communication Systems	Design for Testability	Machine Modeling and Analysis	Mechanical Vibrations	Data Mining
21-07-2017 Friday	Network Security and Cryptography	VLSI Physical Design	Modern Control Theory	Additive Manufacturing	Network Security
24-07-2017 Monday	Audio and Speech Signal Processing	CPLD & FPGA Architectures and Applications	Programmable Logic Controllers and their Applications	Experimental Techniques and Data Analysis	Cloud Computing
26-07-2017 Wednesday	Finishing School-II (10.00 am to 11.30 am)	Finishing School-II (10.00 am to 11.30 am)	Finishing School-II (10.00 am to 11.30 am)	Finishing School-II (10.00 am to 11.30 am)	Finishing School-II (10.00 am to 11.30 am)

Copy to:

Sd/-Controller of Examinations

Date: 17-05-2017

- 1. The Principal, VCE for information.
- 2. The Director, Admissions and Examinations, VCE.
- with a request to bring it to
- The Coordinator, Computer Center, VCE, with a request to upload the timetable in the college website.
- 5. The Librarian, VCE, for information.