

**VASAVI COLLEGE OF ENGINEERING(AUTONOMOUS) HYDERABAD-31**  
**Results of M.E. IV-Semester (Late Submission) Examinations held in November-2017**

**DATE:13-12-2017**

Provisional list of the candidates who have **passed** M.E. IV-Semester Examination in FIRST DIVISION

**M.E. (E.C.E) : Embedded Systems and VLSI Design**

1602-14-744-004      1602-15-744-010

**M.E. (E.C.E.) : Communication Engineering & Signal Processing**

1602-15-744-106

**M.E. (E.E.E) Power Systems & Power Electronics**

1602-15-766-011

The candidates may collect their Semester Grade Report, Consolidated Grade Report and Provisional certificate from the O/o the Director (Admissions and Examinations), Ramanujan Block, VCE **on or after 20<sup>th</sup> December, 2017.**

**Sd-**

**Controller of Examinations**

**Vasavi of College of Engineering**

**Sd-**

**Principal**

**Vasavi of College of Engineering**