

VASAVI COLLEGE OF ENGINEERING (Autonomous), IBRAHIMBAGH, HYDERABAD-31
M.E./M.Tech. (CBCS) I-Semester Make-up Examinations, March-2018

No. 68/CoE/2018

TIME TABLE

Date: 23-02-2018

Timings: 10.00 am to 1.00 pm

DATE / DAY	E.C.E.		E.E.E.	MECHANICAL	C.S.E.
	Communication Engineering & Signal Processing	Embedded Systems & VLSI Design	Power Systems & Power Electronics	Advanced Design & Manufacturing	
05.03.2018 Monday	Image and Video Processing	Embedded Systems Design	Application of Power Electronics to Power Systems	Mathematical Methods for Engineers	---
07.03.2018 Wednesday	Advanced Digital Signal Processing	Digital IC Design	Power System Stability	Metal Cutting and Forming	---
09.03.2018 Friday	---	Analog IC Design	Power Electronic Converters	Computer Integrated Design and Manufacturing	Advanced Algorithms
12.03.2018 Monday	Data and Computer Communication Networks	Physics of Semiconductor Devices	High Voltage DC Transmission	Finite Element Techniques	---
14.03.2018 Wednesday	Adaptive Signal Processing	Advanced Computer Organization	Renewable Energy Sources	Theory of Elasticity and Plasticity	---
16.03.2018 Friday	Audio and Speech Signal Processing	VLSI Technology	Power Quality Engineering	Advanced Non-Destructive Evaluation Techniques	---
19.03.2018 Monday	Finishing School-I (10.00 am to 11.30 am)	Finishing School-I (10.00 am to 11.30 am)	Finishing School-I (10.00 am to 11.30 am)	Finishing School-I (10.00 am to 11.30 am)	Finishing School-I (10.00 am to 11.30 am)

Note: Omission (s) if any, may immediately be brought to the notice of the undersigned

Sd/-

Controller of Examinations

Copy to:

1. The Principal, VCE for information.
 2. The Director, Admissions and Examinations, VCE.
 3. The Head, Department of CSE/ECE/EEE/Mech. Engg./H&SS/Mathematics, VCE.
 4. The Coordinator, Computer Centre, VCE, with a request to upload the timetable in the college website.
 5. The Librarian, VCE for information.
- } with a request to bring it to
} the notice of all concerned.