

**VASAVI COLLEGE OF ENGINEERING (Autonomous)**  
**Ibrahimbagh, Hyderabad – 500 031**

No. 958/B.E./ Exams/ TT/2017

Date: 20-10-2017

B.E. (CBCS) I-Semester (Main & Backlog) and I Year I-Semester Backlog • Examinations,  
December-2017/January-2018

**TIME TABLE**

Timings: **2.00 pm to 5.00 pm**

DATE/ DAY	COURSE
22.12.17 Friday	<b>N, O</b> : English-I
27.12.17 Wednesday	<b>N, O</b> : Engineering Mathematics-I
29.12.17 Friday	<b>N, O</b> : Engineering Physics
02.01.18 Tuesday	<b>N, O</b> : Engineering Chemistry
04.01.18 Thursday	<b>N</b> : Computer Programming <b>O</b> : Computer Programming and Problem Solving using C
06.01.18 Saturday	<b>N, O</b> : Basic Engineering Mechanics (Civil, EEE & Mech.) <b>N, O</b> : Basic Electrical Engineering (CSE, ECE & IT)
08.01.18 Monday	<b>N</b> : Engineering Graphics-I (Civil, EEE & Mech) <b>O</b> : Engineering Graphics-I
10.01.18 Wednesday	<b>N</b> : Engineering Drawing-I (CSE, ECE & IT)

● *Candidates of 2014-15 & 2015-16 batches, having backlogs in B.E. I year I Semester are required to register and write I-Semester (CBCS) backlog examinations.*

**Note:** Omissions if any, may be immediately brought to the notice of the undersigned.

**Sd/-**  
**Controller of Examinations**

**Copy to:**

1. The Principal, VCE for Information.
2. The Director, Admissions and Examinations, VCE, with a request to bring it to the notice of all concerned.
3. The Head, Dept. of H&SS/ Mathematics/ Physics/ Chemistry/ Civil/ CSE/ EEE/ ECE/ Mech./ IT, VCE.
4. The Coordinator, Computer Centre, VCE with request to upload the time table in the college website.
5. The Librarian, VCE for information.