



ONLINE FACULTY DEVELOPMENT PROGRAMME (FDP)

On

Emerging Trends on High-Speed Analog and Mixed-Signal circuit Design

(2nd June – 11th June 2025)

Organized by

Electronics & ICT Academy, NIT Warangal

In Association With

Department of Electronics and Communication Engineering,

Vasavi College of Engineering (Autonomous)

(Accredited by NAAC with A++ Grade, Affiliated to Osmania University),

Ibrahim Bagh, Hyderabad, 500031



Preamble

"Electronics & ICT Academy – Phase II" was set up at NIT Warangal with financial assistance from MeitY, GoI. This academy's role is to offer Faculty Development Programmes in standardized courses and emerging areas of Electronics, Information Communication Technologies, training & consultancy services for Industry, Curriculum development for Industry, CEP for working professionals, Advice and support for technical incubation and entrepreneurial activities.

About the FDP

This Faculty Development Program (FDP) will explore advanced design techniques and innovation addressing the challenges of high-frequency applications, it will cover topics like low power design, noise reduction, high-speed data conversion and signal integrity. This program aims to enhance knowledge and skills of faculty members and researchers in designing efficient analog and mixed signal circuits for modern high-speed system with the input case studies of industry experts.

Major Course Contents

- Review of basic building blocks of Analog circuits
- High speed op-amp with compensation techniques
- High performance dynamic comparators
- Clock generation for high-speed circuits
- Design of high-speed ADCs
- Evaluating and mitigating noise in Analog circuits
- Low drop out (LDO) regulators - design tradeoffs
- Testing of VLSI digital circuits
- Hands-on training with Cadence for simulation, layout, and verification of analog and digital circuits.

Faculty conducting this Programme

The programme will be conducted by the faculty members from NIT Warangal and ECE Department Vasavi College of Engineering. Resource persons are invited from IITs / NITs to deliver lectures in the programme. Experts from industries also will deliver lectures as a part of the course.

Registration Fee Particulars

Faculty and Research Scholars	Rs. 750 /-
Industry Participants	Rs. 2250 /-

Participants need to pay the Registration Fee Online using the following details

Online Transfer Details

Account Name: Electronics & ICT Academy NITW
Account No : 62423775910
IFSC : SBIN0020149
Bank and Branch: State Bank of India, NIT(REC) Warangal

How to apply

Participants are required to fill out the online registration form by clicking on the following link:

<https://forms.gle/tALUYFqrs8yxEviU6>

Selection Criteria

Selection will be made based on a first-come-first-serve basis to a maximum of 50 (fifty). Candidates will be issued satisfactory certificates on successful completion of the course.

Important Dates

Last date (Application)	28.05.2025
Selection List by E- mail	29.05.2025
Duration	02.06.2025 to 11.06.2025

About NIT Warangal

National Institute of Technology, Warangal, is the first among 17 RECs set up as a joint venture of the Government of India and the state government. Over the years, the college has established itself as a premier Institute imparting technical education of a very high standard, leading to B.Tech degrees in various branches of engineering, M.Tech., and Ph.D. programmes in various specializations.

About Vasavi College of Engineering

Established in 1981 by Vasavi Academy of Education, VCE represents a rich tradition of excellence in technology-based education. A premier-league institution among the affiliates of Osmania University, The college is accredited by NAAC with 'A++' GRADE also offers seven UG (Civil, Mechanical, ECE, CSE, CSE AI & ML, EEE and IT) with the total sanctioned intake of 780 and 4 PG programmes [ME (ES & VLSI); ME (ADM); ME(PS & PE); and M.Tech (CSE)]. The college has been granted autonomy by the University Grants Commission, New Delhi and Osmania University, for all the programs for a period of six years with effect from 2014-15 and has been extended the autonomous status for a period of ten years from 2021-2022 to 2030-2031. All the 5 UG programs (Civil, CSE, ECE, EEE & Mech. Engg.) got NBA accreditation under Tier-I for a period of 3 years with effect from 2024-27.

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1. Name :
2. Designation :
3. Institution :
4. Email :
5. Mobile No :

6. Reference No:
Amount:
Bank:
Date:

7. Address for Correspondence:
8. Educational Qualification:
9. Subjects taught so far:
10. No. of refresher courses/workshops attended:
11. Experience (in years):
12. Teaching: Research: Industry:
13. Do you belong to SC/ST: YES/NO

Declaration

The information provided is true to the best of my knowledge. If selected, I agree to abide by the rules and regulations of the FDP and shall attend the course for the entire duration. I also undertake the responsibility to inform the Coordinator in case, I am unable to attend the course.

Date:

Place:

Signature of the Applicant

SPONSORSHIP CERTIFICATE

Dr./Mr./Ms Is an employee of our Institute/Organization and is hereby sponsored to participate in the FDP on "**Emerging Trends on High-Speed Analog and Mixed-Signal circuit Design**", sponsored by Electronics & ICT Academy during **02nd June 2025-11th June 2025**.

**Signature of Head of Institution
(with seal)**

It is requested to all the delegates, after completion of application form, all the payment details, screenshot of the payment and scanned application form has to be uploaded in the following online registration link.

Registration Link:

<https://forms.gle/tALUYFqrs8yxEviU6>

For more details about Electronics & ICT Academy, NIT, Warangal, please visit:

<https://nitw.ac.in/eict>

For more enquiries please contact:

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