

**VASAVI COLLEGE OF ENGINEERING (AUTONOMOUS)**

**IBRAHIMBAGH: HYDERABAD-31**

Date: 13-12-2021

**Time-Table for B.E. V Semester (Main/Suppl.) Practical Examinations**

**Academic year 2021-2022**

**CIVIL**

**Hydraulics & Hydraulics Machinery Lab**

DATES	9.00 AM TO 12.00 NOON	12.00 NOON TO 3.00 PM	3.00 PM TO 6.00PM
28.12.2021	A1	A2	A3
29.12.2021	A4	A5	A6

**Environmental Engineering Lab**

DATES	9.00 AM TO 12.00 NOON	12.00 NOON TO 3.00 PM	3.00 PM TO 6.00PM
30.12.2021	A1	A2	A3
31.12.2021	A4	A5	A6

**Concrete Lab**

DATES	9.00 AM TO 12.00 NOON	12.00 NOON TO 3.00 PM	3.00 PM TO 6.00PM
28.12.2021	A4	A5	A6
29.12.2021	A1	A2	A3

**BATCHES:-**

A1→	1602-19-732-001 TO 009, 011, 012, 014;	12
A2→	1602-19-732-015 TO 1602-19-732-026;	12
A3→	1602-19-732-027 TO 1602-19-732-038;	12
A4→	1602-19-732-039 TO 040, 042 TO 1602-19-732-051;	12
A5→	1602-19-732-052 TO 1602-19-732-062;	11
A6→	1602-19-732-301 TO 1602-19-732-307; + ASRC	

(\*) ASRC: Any Supplementary Registered Candidates

Sd/-  
**CHIEF SUPERINTENDENT**

**VASAVI COLLEGE OF ENGINEERING (AUTONOMOUS)**

**IBRAHIMBAGH: HYDERABAD-31**

Date: 13-12-2021

**Time-Table for B.E. V Semester (Main/Suppl.) Practical Examinations**  
**Academic year 2021-2022**

**CSE (SECTION-A)**

**DBMS Lab**

DATES	9.40 AM TO 12.40 PM	1.00 PM TO 4.00 PM
28.12.2021	A1	A2
29.12.2021	A3	A4

**Computer Networks Lab**

DATES	9.40 AM TO 12.40 PM	1.00 PM TO 4.00 PM
30.12.2021	A1	A2
31.12.2021	A3	A4

**Web Programming Lab**

DATES	9.40 AM TO 12.40 PM	1.00 PM TO 4.00 PM
28.12.2021	A3	A4
29.12.2021	A1	A2

**BATCHES: -**

A1→	1602-19-733-001 TO 1602-19-733-018;	17
A2→	1602-19-733-019 TO 1602-19-733-036;	18
A3→	1602-19-733-037 TO 1602-19-733-054;	18
A4→	1602-19-733-055 TO 1602-19-733-061; 183; 1602-19-733-301 TO 306;+ ASRC	

(\*) ASRC: Any Supplementary Registered Candidates

Sd/-  
**CHIEF SUPERINTENDENT**

**VASAVI COLLEGE OF ENGINEERING (AUTONOMOUS)  
IBRAHIMBAGH: HYDERABAD-31**

Date: 13-12-2021

**Time-Table for B.E. V Semester (Main/Suppl.) Practical Examinations**  
**Academic year 2021-2022**

**CSE (SECTION-B)**

**DBMS Lab**

DATES	9.40 AM TO 12.40 PM	1.00 PM TO 4.00 PM
30.12.2021	B1	B2
31.12.2021	B3	B4

**Computer Networks Lab**

DATES	9.40 AM TO 12.40 PM	1.00 PM TO 4.00 PM
28.12.2021	B1	B2
29.12.2021	B3	B4

**Web Programming Lab**

DATES	9.40 AM TO 12.40 PM	1.00 PM TO 4.00 PM
30.12.2021	B3	B4
31.12.2021	B1	B2

**BATCHES: -**

B1→	1602-19-733-062 TO 1602-19-733-079;	18
B2→	1602-19-733-080 TO 1602-19-733-097;	18
B3→	1602-19-733-098 TO 1602-19-733-115;	18
B4→	1602-19-733-116 TO 118, 120, 121; 184; 18-733-101; 1602-19-733-307 TO 312;+ ASRC	

(\*) ASRC: Any Supplementary Registered Candidates

Sd/-  
**CHIEF SUPERINTENDENT**

**VASAVI COLLEGE OF ENGINEERING (AUTONOMOUS)  
IBRAHIMBAGH: HYDERABAD-31**

Date: 13-12-2021

**Time-Table for B.E. V Semester (Main/Suppl.) Practical Examinations**  
**Academic year 2021-2022**

**CSE (SECTION-C)**

**DBMS Lab**

DATES	9.40 AM TO 12.40 PM	1.00 PM TO 4.00 PM
28.12.2021	C1	C2
29.12.2021	C3	C4

**Computer Networks Lab**

DATES	9.40 AM TO 12.40 PM	1.00 PM TO 4.00 PM
28.12.2021	C3	C4
29.12.2021	C1	C2

**Web Programming Lab**

DATES	9.40 AM TO 12.40 PM	1.00 PM TO 4.00 PM
30.12.2021	C1	C2
31.12.2021	C3	C4

**BATCHES: -**

C1→	1602-19-733-122 TO 1602-19-733-138;	18
C2→	1602-19-733-139 TO 1602-19-733-157;	18
C3→	1602-19-733-158 TO 1602-19-733-175;	18
C4→	1602-19-733-176 TO 182; 1602-19-733-313 TO 1602-19-733-318;+ ASRC	

(\*) ASRC: Any Supplementary Registered Candidates

Sd/-  
**CHIEF SUPERINTENDENT**



**VASAVI COLLEGE OF ENGINEERING (AUTONOMOUS)**

**IBRAHIMBAGH: HYDERABAD-31**

Date: 13-12-2021

**Time-Table for B.E. V Semester (Main/Suppl.) Practical Examinations**  
**Academic year 2021-2022**

**EEE**

**Linear Integrated Circuits & Digital Electronics Lab**

DATES	9.00 AM TO 12.00 NOON	12.00 NOON TO 3.00 PM	3.00 PM TO 6.00PM
28.12.2021	A1	A2	A3
29.12.2021	A4	A5	A6

**Microprocessors & Microcontrollers Applications Lab**

DATES	9.00 AM TO 12.00 NOON	12.00 NOON TO 3.00 PM	3.00 PM TO 6.00PM
05.01.2022	A1	A2	A3
06.01.2022	A4	A5	A6

**AC Machines Lab**

DATES	9.00 AM TO 12.00 NOON	12.00 NOON TO 3.00 PM	3.00 PM TO 6.00PM
06.01.2022	A1	A2	A3
07.01.2022	A4	A5	A6

**BATCHES:-**

A1→	1602-19-734-001 TO 1602-19-734-011;	11
A2→	1602-19-734-012 TO 1602-19-734-022;	11
A3→	1602-19-734-023 TO 1602-19-734-033;	11
A4→	1602-19-734-034 TO 1602-19-734-044;	11
A5→	1602-19-734-045 TO 1602-19-734-055;	11
A6→	1602-19-734-056 TO 060; 1602-19-734-301 TO 1602-19-734-306; + ASRC	

(\*) ASRC: Any Supplementary Registered Candidates

Sd/-  
**CHIEF SUPERINTENDENT**

**VASAVI COLLEGE OF ENGINEERING (AUTONOMOUS)  
IBRAHIMBAGH: HYDERABAD-31**

Date: 13-12-2021

**Time-Table for B.E V Semester (Main/Suppl.) Practical Examinations  
Academic year 2021-2022**

**ECE (SECTION-A)**

**Control Systems Engineering Lab**

DATES	9.00 AM TO 12.00 NOON	12.00 NOON TO 3.00 PM	3.00 PM TO 6.00PM
28.12.2021	A1	A2	A3
29.12.2021	A4	A5	A6

**Integrated Circuits & Applications Lab**

DATES	9.00 AM TO 12.00 NOON	12.00 NOON TO 3.00 PM	3.00 PM TO 6.00PM
06.01.2022	A1	A2	A3
07.01.2022	A4	A5	A6

**Analog & Digital Communication Lab**

DATES	9.00 AM TO 12.00 NOON	12.00 NOON TO 3.00 PM	3.00 PM TO 6.00PM
03.01.2022	A1	A2	A3
04.01.2022	A4	A5	A6

**BATCHES:-**

A1→	1602-19-735-001 TO 1602-19-735-012;	12
A2→	1602-19-735-013 TO 1602-19-735-024;	12
A3→	1602-19-735-025 TO 028, 030 TO 1602-19-735-037;	12
A4→	1602-19-735-038 TO 1602-19-735-049;	12
A5→	1602-19-735-050 TO 1602-19-735-060;	11
A6→	1602-19-735-301 TO 1602-19-735-306; + ASRC	

(\*) ASRC: Any Supplementary Registered Candidates

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**CHIEF SUPERINTENDENT**

**VASAVI COLLEGE OF ENGINEERING (AUTONOMOUS)**  
**IBRAHIMBAGH: HYDERABAD-31**

Date: 13-12-2021

**Time-Table for B.E V Semester (Main/Suppl.) Practical Examinations**  
**Academic year 2021-2022**

**ECE (SECTION-B)**

**Control Systems Engineering Lab**

DATES	9.00 AM TO 12.00 NOON	12.00 NOON TO 3.00 PM	3.00 PM TO 6.00PM
30.12.2021	B1	B2	B3
31.12.2021	B4	B5	B6

**Integrated Circuits & Applications Lab**

DATES	9.00 AM TO 12.00 NOON	12.00 NOON TO 3.00 PM	3.00 PM TO 6.00PM
03.01.2022	B1	B2	B3
04.01.2022	B4	B5	B6

**Analog & Digital Communication Lab**

DATES	9.00 AM TO 12.00 NOON	12.00 NOON TO 3.00 PM	3.00 PM TO 6.00PM
28.12.2021	B1	B2	B3
29.12.2021	B4	B5	B6

**BATCHES:-**

B1→	1602-19-735-061 TO 1602-19-735-072;	12
B2→	1602-19-735-073 TO 1602-19-735-084;	12
B3→	1602-19-735-085 TO 1602-19-735-096;	12
B4→	1602-19-735-097 TO 107, 109;	12
B5→	1602-19-735-110 TO 1602-19-735-120;	11
B6→	1602-19-735-307 TO 1602-19-735-312; + ASRC	

(\*) ASRC: Any Supplementary Registered Candidates

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**CHIEF SUPERINTENDENT**



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IBRAHIMBAGH: HYDERABAD-31**

Date: 13-12-2021

**Time-Table for B.E V Semester (Main/Suppl.) Practical Examinations**  
**Academic year 2021-2022**

**ECE (SECTION-C)**

**Control Systems Engineering Lab**

DATES	9.00 AM TO 12.00 NOON	12.00 NOON TO 3.00 PM	3.00 PM TO 6.00PM
03.01.2022	C1	C2	C3
04.01.2022	C4	C5	C6

**Integrated Circuits & Applications Lab**

DATES	9.00 AM TO 12.00 NOON	12.00 NOON TO 3.00 PM	3.00 PM TO 6.00PM
30.12.2021	C1	C2	C3
31.12.2021	C4	C5	C6

**Analog & Digital Communication Lab**

DATES	9.00 AM TO 12.00 NOON	12.00 NOON TO 3.00 PM	3.00 PM TO 6.00PM
06.01.2022	C1	C2	C3
07.01.2022	C4	C5	C6

**BATCHES:-**

C1→	1602-19-735-121 TO 1602-19-735-132;	12
C2→	1602-19-735-133 TO 1602-19-735-144;	12
C3→	1602-19-735-145 TO 1602-19-735-156;	12
C4→	1602-19-735-158 TO 1602-19-735-169;	12
C5→	1602-19-735-170 TO 1602-19-735-180;	11
C6→	1602-19-735-313 TO 1602-19-735-318; + ASRC	

(\*) ASRC: Any Supplementary Registered Candidates

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Date: 13-12-2021

**Time-Table for B.E. V Semester (Main/Suppl.) Practical Examinations**  
**Academic year 2021-2022**

**MECH-A**

**Dynamics and Metrology Lab**

DATES	9.30 AM TO 12.30 PM	1.00 PM TO 4.00 PM
28.12.2021	A1	A2
29.12.2021	A3	A4

**Manufacturing Processes Lab**

DATES	9.30 AM TO 12.30 PM	1.00 PM TO 4.00 PM
30.12.2021	A1	A2
31.12.2021	A3	A4

**BATCHES:-**

A1→	1602-19-736-001 TO 1602-19-736-018;	18
A2→	1602-19-736-019 TO 020, 022 TO 1602-19-736-037;	18
A3→	1602-19-736-038 TO 1602-19-736-055;	18
A4→	1602-19-736-056; 1602-19-736-301 TO 1602-19-736-306, 313, 314, 315 + ASRC	

(\*) ASRC: Any Supplementary Registered Candidates

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**CHIEF SUPERINTENDENT**

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Date: 13-12-2021

**Time-Table for B.E. V Semester (Main/Suppl.) Practical Examinations**  
**Academic year 2021-2022**

**MECH-B**

**Dynamics and Metrology Lab**

DATES	9.30 AM TO 12.30 PM	1.00 PM TO 4.00 PM
30.12.2021	B1	B2
31.12.2021	B3	B4

**Manufacturing Processes Lab**

DATES	9.30 AM TO 12.30 PM	1.00 PM TO 4.00 PM
28.12.2021	B1	B2
29.12.2021	B3	B4

**BATCHES:-**

B1→	1602-19-736-057 TO 1602-19-736-074;	18
B2→	1602-19-736-075 TO 1602-19-736-092;	18
B3→	1602-19-736-093 TO 1602-19-736-110;	18
B4→	1602-19-736-111 TO 1602-19-736-114; 18-736-088, 099, 1602-19-736-307 TO 1602-19-736-312 + ASRC	

(\*) ASRC: Any Supplementary Registered Candidates

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Date: 13-12-2021

**Time-Table for B.E. V Semester (Main/Suppl.) Practical Examinations**  
**Academic year 2021-2022**

**I.T (SECTION-A)**

**Computers Networks Lab**

DATES	9.40 AM TO 12.40 PM	1.20 PM TO 4.20 PM
30.12.2021	A1	A2
31.12.2021	A3	A4

**Microprocessor & Interfacing Lab**

DATES	9.40 AM TO 12.40 PM	1.20 PM TO 4.20 PM
03.01.2022	A1	A2
04.01.2022	A3	A4

**Operating Systems Lab**

DATES	9.40 AM TO 12.40 PM	1.20 PM TO 4.20 PM
28.12.2021	A1	A2
29.12.2021	A3	A4

**BATCHES:-**

A1→	1602-19-737-001, 003 TO 1602-19-737-019;	18
A2→	1602-19-737-020 TO 1602-19-737-037;	18
A3→	1602-19-737-038 TO 1602-19-737-055;	18
A4→	1602-19-737-056 TO 061, 183; 1602-19-737-301 TO 1602-19-737-306; +ASRC	

(\*) ASRC: Any Supplementary Registered Candidates

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Date: 13-12-2021

**Time-Table for B.E. V Semester (Main/Suppl.) Practical Examinations**  
**Academic year 2021-2022**

**I.T (SECTION-B)**

**Computers Networks Lab**

DATES	9.40 AM TO 12.40 PM	1.20 PM TO 4.20 PM
28.12.2021	B1	B2
29.12.2021	B3	B4

**Microprocessor & Interfacing Lab**

DATES	9.40 AM TO 12.40 PM	1.20 PM TO 4.20 PM
30.12.2021	B1	B2
31.12.2021	B3	B4

**Operating Systems Lab**

DATES	9.40 AM TO 12.40 PM	1.20 PM TO 4.20 PM
03.01.2022	B1	B2
04.01.2022	B3	B4

**BATCHES:-**

B1→	1602-19-737-062 TO 070, 072 TO 1602-19-737-080;	18
B2→	1602-19-737-081 TO 084, 086 TO 1602-19-737-099;	18
B3→	1602-19-737-100, 101, 103 TO 1602-19-737-118;	18
B4→	1602-19-737-119 TO 122; 184; 1602-18-737-085; 1602-19-737-307 TO 312+ASRC	

(\*) ASRC: Any Supplementary Registered Candidates

Sd/-  
**CHIEF SUPERINTENDENT**

**VASAVI COLLEGE OF ENGINEERING (AUTONOMOUS)**

**IBRAHIMBAGH: HYDERABAD-31**

Date: 13-12-2021

**Time-Table for B.E. V Semester (Main/Suppl.) Practical Examinations**  
**Academic year 2021-2022**

**I.T (SECTION-C)**

**Computers Networks Lab**

DATES	9.40 AM TO 12.40 PM	1.20 PM TO 4.20 PM
03.01.2022	C1	C2
04.01.2022	C3	C4

**Microprocessor & Interfacing Lab**

DATES	9.40 AM TO 12.40 PM	1.20 PM TO 4.20 PM
28.12.2021	C1	C2
29.12.2021	C3	C4

**Operating Systems Lab**

DATES	9.40 AM TO 12.40 PM	1.20 PM TO 4.20 PM
30.12.2021	C1	C2
31.12.2021	C3	C4

**BATCHES:-**

C1→	1602-19-737-123 TO 1602-19-737-139, 141;	18
C2→	1602-19-737-142 TO 1602-19-737-159;	18
C3→	1602-19-737-160 TO 1602-19-737-177;	18
C4→	1602-19-737-178 TO 1602-19-737-182; 1602-19-737-313 TO 318; +ASRC	

(\*) ASRC: Any Supplementary Registered Candidates

Sd/-  
**CHIEF SUPERINTENDENT**

**VASAVI COLLEGE OF ENGINEERING (AUTONOMOUS)**

**IBRAHIMBAGH: HYDERABAD-31**

Date: 13-12-2021

**Time-Table for B.E. VII Semester (Main/Suppl.) Practical Examinations**  
**Academic year 2021-2022**

**CIVIL**

**Geographical Information System Lab**

DATES	9.00 AM TO 12.00 NOON	12.00 NOON TO 3.00 PM	3.00 PM TO 6.00PM
06.01.2022	A1	A2	A3
07.01.2022	A4	A5	A6

**BATCHES: -**

A1→	1602-18-732-001 TO 1602-18-732-012;	12
A2→	1602-18-732-013 TO 1602-18-732-024;	12
A3→	1602-18-732-026 TO 1602-18-732-037;	12
A4→	1602-18-732-038, 040 TO 1602-18-732-050;	12
A5→	1602-18-732-051 TO 1602-18-732-062;	12
A6→	1602-18-732-301 TO 1602-18-732-306; 1602-17-732-043; +ASRC	

(\*) ASRC: Any Supplementary Registered Candidates

Sd/-  
**CHIEF SUPERINTENDENT**

**VASAVI COLLEGE OF ENGINEERING (AUTONOMOUS)  
IBRAHIMBAGH: HYDERABAD-31**

Date: 13-12-2021

**Time-Table for B.E. VII Semester (Main/Suppl.) Practical Examinations**  
**Academic year 2021-2022**

**CSE (SECTION-A)**

**Distributed Systems and Cloud Computing Lab**

DATES	9.40 AM TO 12.40 PM	1.00 PM TO 4.00 PM
28.12.2021	A1	A2
29.12.2021	A3	A4

**Compiler Construction Lab**

DATES	9.40 AM TO 12.40 PM	1.00 PM TO 4.00 PM
30.12.2021	A1	A2
31.12.2021	A3	A4

**BATCHES:**

A1→	1602-18-733-001 TO 008, 010 TO 019;	18
A2→	1602-18-733-020 TO 1602-18-733-037;	18
A3→	1602-18-733-038 TO 045, 047 TO 056;	18
A4→	1602-18-733-057 TO 1602-18-733-061; 1602-18-733-301 TO 306; + ASRC	

(\*) ASRC: Any Supplementary Registered Candidates

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**CHIEF SUPERINTENDENT**



**VASAVI COLLEGE OF ENGINEERING (AUTONOMOUS)  
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Date: 13-12-2021

**Time-Table for B.E. VII Semester (Main/Suppl.) Practical Examinations**  
**Academic year 2021-2022**

**CSE (SECTION-B)**

**Distributed Systems and Cloud Computing Lab**

DATES	9.40 AM TO 12.40 PM	1.00 PM TO 4.00 PM
30.12.2021	B1	B2
31.12.2021	B3	B4

**Compiler Construction Lab**

DATES	9.40 AM TO 12.40 PM	1.00 PM TO 4.00 PM
28.12.2021	B1	B2
29.12.2021	B3	B4

**BATCHES:**

A1→	1602-18-733-062 TO 1602-18-733-079;	18
A2→	1602-18-733-080 TO 1602-18-733-097;	18
A3→	1602-18-733-098 TO 100, 102 TO 116;	18
A4→	1602-18-733-117 TO 121; 1602-18-733-307 TO 312;+ ASRC	

(\*) ASRC: Any Supplementary Registered Candidates

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**CHIEF SUPERINTENDENT**

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Date: 13-12-2021

**Time-Table for B.E. VII Semester (Main/Suppl.) Practical Examinations**  
**Academic year 2021-2022**

**EEE**

**Power Systems Lab**

DATES	9.00 AM TO 12.00 NOON	12.00 NOON TO 3.00 PM	3.00 PM TO 6.00PM
29.12.2021	A1	A2	A3
30.12.2021	A4	A5	A6

**BATCHES:-**

A1→	1602-18-734-001 TO 1602-18-734-012;	12
A2→	1602-18-734-013 TO 1602-18-734-024;	12
A3→	1602-18-734-025 TO 1602-18-734-036;	12
A4→	1602-18-734-037 TO 1602-18-734-048;	12
A5→	1602-18-734-049 TO 055, 057 TO 060;	11
A6→	1602-17-734-005, 050, 055; 1602-18-734-301 TO 306; + ASRC	

**Digital Signal Processing Lab**

DATES	9.40 AM TO 12.40 PM	1.20 PM TO 4.20 PM
04.01.2022	A1	A2
05.01.2022	A3	A4

**BATCHES:-**

A1→	1602-18-734-001 TO 1602-18-734-017;	17
A2→	1602-18-734-018 TO 1602-18-734-034;	17
A3→	1602-18-734-035 TO 1602-18-734-051;	17
A4→	1602-18-734-052 TO 1602-18-734-060; 1602-17-734-005, 050, 055; 1602-18-734-301 TO 306; + ASRC	

(\*) ASRC: Any Supplementary Registered Candidates

Sd/-  
**CHIEF SUPERINTENDENT**

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Date: 13-12-2021

**Time-Table for B.E. VII Semester (Main/Suppl.) Practical Examinations**  
**Academic year 2021-2022**

**ECE (SECTION-A)**

**Microwave Engineering Lab**

DATES	9.00 AM TO 12.00 NOON	12.00 NOON TO 3.00 PM	3.00 PM TO 6.00PM
28.12.2021	A1	A2	A3
29.12.2021	A4	A5	A6

**VLSI Design Lab**

DATES	9.00 AM TO 12.00 NOON	12.00 NOON TO 3.00 PM	3.00 PM TO 6.00PM
03.01.2022	A1	A2	A3
04.01.2022	A4	A5	A6

**BATCHES:-**

A1→	1602-18-735-001 TO 1602-18-735-012;	12
A2→	1602-18-735-013 TO 1602-18-735-024;	12
A3→	1602-18-735-025 TO 1602-18-735-036;	12
A4→	1602-18-735-037 TO 1602-18-735-048;	12
A5→	1602-18-735-049 TO 1602-18-735-060;	12
A6→	1602-18-735-301 TO 1602-18-735-306; + ASRC	

(\*) ASRC: Any Supplementary Registered Candidates

Sd/-  
**CHIEF SUPERINTENDENT**

**VASAVI COLLEGE OF ENGINEERING (AUTONOMOUS)  
IBRAHIMBAGH: HYDERABAD-31**

Date: 13-12-2021

**Time-Table for B.E. VII Semester (Main/Suppl.) Practical Examinations**  
**Academic year 2021-2022**

**ECE (SECTION-B)**

**Microwave Engineering Lab**

DATES	9.00 AM TO 12.00 NOON	12.00 NOON TO 3.00 PM	3.00 PM TO 6.00PM
30.12.2021	B1	B2	B3
31.12.2021	B4	B5	B6

**VLSI Design Lab**

DATES	9.00 AM TO 12.00 NOON	12.00 NOON TO 3.00 PM	3.00 PM TO 6.00PM
05.01.2022	B1	B2	B3
06.01.2022	B4	B5	B6

**BATCHES:-**

B1→	1602-18-735-061 TO 1602-18-735-072;	12
B2→	1602-18-735-073 TO 1602-18-735-084;	12
B3→	1602-18-735-085 TO 1602-18-735-096;	12
B4→	1602-18-735-097 TO 1602-18-735-108;	12
B5→	1602-18-735-109 TO 1602-18-735-120;	12
B6→	1602-18-735-307 TO 1602-18-735-312;+ ASRC	

(\*) ASRC: Any Supplementary Registered Candidates

Sd/-  
**CHIEF SUPERINTENDENT**

**VASAVI COLLEGE OF ENGINEERING (AUTONOMOUS)**

**IBRAHIMBAGH: HYDERABAD-31**

Date: 13-12-2021

**Time-Table for B.E. VII Semester (Main/Suppl.) Practical Examinations**  
**Academic year 2021-2022**

**MECH (SECTION-A)**

**Thermal Engineering Lab**

DATES	9.30 AM TO 12.30 PM	1.00 PM TO 4.00 PM
28.12.2021	A1	A2
29.12.2021	A3	A4

**Computer Aided Engineering Lab**

DATES	9.30 AM TO 12.30 PM	1.00 PM TO 4.00 PM
30.12.2021	A1	A2
31.12.2021	A3	A4

**BATCHES:-**

A1→	1602-18-736-001 TO 1602-18-736-018;	18
A2→	1602-18-736-019 TO 1602-18-736-036;	18
A3→	1602-18-736-037 TO 1602-18-736-054;	18
A4→	1602-18-736-055 TO 1602-18-736-059; 17-736-008, 042; 1602-18-736-301 TO 1602-18-736-306; 17-736-305; + ASRC	

(\*) ASRC: Any Supplementary Registered Candidates

Sd/-  
**CHIEF SUPERINTENDENT**

**VASAVI COLLEGE OF ENGINEERING (AUTONOMOUS)**

**IBRAHIMBAGH: HYDERABAD-31**

Date: 13-12-2021

**Time-Table for B.E. VII Semester (Main/Suppl.) Practical Examinations**  
**Academic year 2021-2022**

**MECH (SECTION-B)**

**Thermal Engineering Lab**

DATES	9.30 AM TO 12.30 PM	1.00 PM TO 4.00 PM
30.12.2021	B1	B2
31.12.2021	B3	B4

**Computer Aided Engineering Lab**

DATES	9.30 AM TO 12.30 PM	1.00 PM TO 4.00 PM
28.12.2021	B1	B2
29.12.2021	B3	B4

**BATCHES:-**

B1→	1602-18-736-061 TO 068, 070 TO 074, 076 TO 079;	17
B2→	1602-18-736-081 TO 087, 089 TO 093, 096 TO 098,	15
B3→	1602-18-736-100 TO 114, 116;	15
B4→	1602-18-736-117 TO 1602-18-736-120; 1602-17-736-065; 1602-16-736-082; 1602-18-736-307 TO 312; + ASRC	

(\*) ASRC: Any Supplementary Registered Candidates

Sd/-  
**CHIEF SUPERINTENDENT**

**VASAVI COLLEGE OF ENGINEERING (AUTONOMOUS)**

**IBRAHIMBAGH: HYDERABAD-31**

Date: 13-12-2021

**Time-Table for B.E. VII Semester (Main/Suppl.) Practical Examinations**  
**Academic year 2021-2022**

**I.T (SECTION-A)**

**Compiler Construction Lab**

DATES	9.40 AM TO 12.40 PM	1.20 PM TO 4.20 PM
30.12.2021	A1	A2
31.12.2021	A3	A4

**Distributed Systems & Cloud Computing Lab**

DATES	9.40 AM TO 12.40 PM	1.20 PM TO 4.20 PM
28.12.2021	A1	A2
29.12.2021	A3	A4

**BATCHES:-**

A1→	1602-18-737-001 TO 1602-18-737-018;	18
A2→	1602-18-737-019 TO 1602-18-737-036;	18
A3→	1602-18-737-037 TO 1602-18-737-054;	18
A4→	1602-18-737-055 TO 1602-18-737-060; 17-737-011, 021; 1602-18-737-301 TO 1602-18-737-305; 1602-17-737-312; +ASRC	

(\*) ASRC: Any Supplementary Registered Candidates

Sd/-  
**CHIEF SUPERINTENDENT**

**VASAVI COLLEGE OF ENGINEERING (AUTONOMOUS)**

**IBRAHIMBAGH: HYDERABAD-31**

Date: 13-12-2021

**Time-Table for B.E. VII Semester (Main/Suppl.) Practical Examinations**  
**Academic year 2021-2022**

**I.T (SECTION-B)**

**Compiler Construction Lab**

DATES	9.40 AM TO 12.40 PM	1.20 PM TO 4.20 PM
28.12.2021	B1	B2
29.12.2021	B3	B4

**Distributed Systems & Cloud Computing Lab**

DATES	9.40 AM TO 12.40 PM	1.20 PM TO 4.20 PM
30.12.2021	B1	B2
31.12.2021	B3	B4

**BATCHES:-**

B1→	1602-18-737-061 TO 1602-18-737-074, 076 TO 079;	18
B2→	1602-18-737-080 TO 084, 086 TO 090, 092 TO 099;	18
B3→	1602-18-737-100 TO 1602-18-737-117;	18
B4→	1602-18-737-118 TO 1602-18-737-120; 1602-18-737-307 TO 310, 312;+ASRC	

(\*) ASRC: Any Supplementary Registered Candidates

Sd/-  
**CHIEF SUPERINTENDENT**