

VASAVI COLLEGE OF ENGINEERING (AUTONOMOUS)
ACCREDITED BY NAAC WITH 'A++' GRADE

DEPARTMENT OF ECE

Date: 22.03.2021

M.E. ECE (CE&SP) I-Semester A.Y. 2020-2021
On Campus - Technical Skills and Laboratory Classes

w.e.f. 24.03.2021 to 03.04.2021

Date	Day	FN : 9.40 a.m. to 12.40 p.m	AN : 1.20 p.m. to 04.20 p.m
24.03.2021	Wednesday	Advance Signal Processing (ASP) Lab	Technical Skills : Mrs. Ch. Neetu
25.03.2021	Thursday	Embedded Systems (ES) Lab	Technical Skills : Mrs. Ch. Neetu
26.03.2021	Friday	ASP Lab	ES Lab
27.03.2021	Saturday	Technical Skills : Mrs. Ch. Neetu	ASP Lab
30.03.2021	Tuesday	Technical Skills : Mr. M. Ramanjaneyulu	ES Lab
31.03.2021	Wednesday	ASP Lab	Technical Skills : Mrs. Ch. Neetu
01.04.2021	Thursday	ES Lab	ASP Lab: Internal Exam
03.04.2021	Saturday	Technical Skills : Internal Exam Mrs. Ch. Neetu	ES Lab : Internal Exam

Name of the Technical Skills course: Mathematical Modeling and Simulation of Signals and Systems

Note: Venue:

1) **Technical Skills** - Room No.: J-309 (DSP Lab).

Faculty: Mrs. Ch. Neetu & Mr. M. Ramanjaneyulu

Lab Staff: Mr. P.V. Krishna Mohan Gupta & Mrs. V. Sudha.

2) **Advanced Signal Processing Lab** - Room No.: J-212 (PG-Communication Lab)

Faculty Name: Mr. B. Uma Mahesh Babu & Mr. M. Ramanjaneyulu)

Lab Staff: Mr. P.V. Krishna Mohan Gupta & Mrs. V. Sudha.

3) **Embedded Systems Lab** - Room No.: J303 (MP&MC Lab)

Faculty Name: Mr. N. Abid Ali Khan & Dr. K. Deepti


Lab Staff : Mr. S. Santhosh Karna & Mr. C. S. Sharma.

Technical Skills :

Sl.No.	Date	Time	Name of the Faculty	Name of the Supporting Staff
1	24.03.2021	1:20 PM to 04:20 PM	Mrs. Ch. Neetu	Mr. P. V. Krishna Mohan Gupta & Mrs. V. Sudha
2	25.03.2021	1:20 PM to 04:20 PM	Mrs. Ch. Neetu	
3	27.03.2021	9:40 AM to 12:40 PM	Mrs. Ch. Neetu	
4	30.03.2021	9:40 AM to 12:40 PM	Mr. M. Ramanjaneyulu	
5	31.03.2021	1:20 PM to 04:20 PM	Mrs. Ch. Neetu	
6	03.04.2021	9:40 AM to 12:40 PM	Mrs. Ch. Neetu	


Time Table Coordinator


HoD-ECE


PRINCIPAL

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Date	Day	FN : 9.40 a.m. to 12.40 p.m	AN : 1.20 p.m. to 04.20 p.m
24.03.2021	Wednesday	Technical Skills : Faculty : Ms. G.R. Padmini	Design Simulation – I (DS-I) Lab
25.03.2021	Thursday	Embedded Systems (ES) Lab	DS – I Lab
26.03.2021	Friday	Technical Skills : Faculty : Ms. G.R. Padmini	ES Lab
27.03.2021	Saturday	Technical Skills : Faculty : Mr. O. Rajesh	DS – I Lab
30.03.2021	Tuesday	DS – I Lab	ES Lab
31.03.2021	Wednesday	Technical Skills : Faculty : Ms. G.R. Padmini	DS – I Lab : Internal Exam
01.04.2021	Thursday	ES Lab	Technical Skills : Faculty : Mr. O. Rajesh
03.04.2021	Saturday	Technical Skills : Internal Exam Faculty : Mr. O. Rajesh	ES Lab : Internal Exam

Name of the Technical Skills course: System Design & FPGA Implementation using Xilinx Vivado Tools.

Note: Venue:

1) Technical Skills - Room No.: J-310 (UG-VLSI Lab)

Faculty: Mr. O. Rajesh & Ms. G.R. Padmini

Lab Staff: Mr. Y. Mallesham & Mr. C. S. Sharma.

2) Design Simulation-I Lab - Room No.: J-312 (PG-VLSI Lab)

Faculty : Dr. N.S.S. Reddy & Mr. R. Goutham

Lab Staff : Mr. Y. Mallesham & Mr. C. S. Sharma

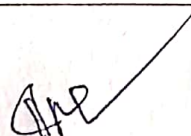
3) Embedded Systems Lab Room No.: J-303 (MP&MC Lab)

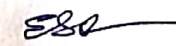
Faculty : Mr. N. Abid Ali Khan & Dr. K. Deepti

Lab Staff : Mr. S. Santhosh Karna & Mr. C. S. Sharma

Technical Skills :

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