

# VASAVI COLLEGE OF ENGINEERING (AUTONOMOUS), IBRAHIMBAGH, HYDERABAD-31

No. 363/M.E./M.Tech./Exams/TT/2020

## TIME TABLE


Date: 10.02.2020

### M.E./M.Tech. (CBCS) I-Semester Makeup Examinations - February-2020

**Timings: 02.00 PM to 05.00 PM**

Date / Day	E.C.E.		E.E.E.	Mech. Engg.	C.S.E.
	Communication Engineering & Signal Processing	Embedded Systems & VLSI Design	Power Systems & Power Electronics	Advanced Design & Manufacturing	
20.02.2020 (Thursday)	Advanced Digital Signal Processing	Embedded System Design	Skill Development Course-I: Communication Skills in English / Technical skills (02.00 PM to 04.00 PM)	Mathematical Methods for Engineers / Metal Cutting and Forming	Skill Development Course-I: Communication Skills in English (02.00 PM to 04.00 PM)
22.02.2020 (Saturday)	Advanced Digital Modulation Techniques	Digital IC Design	Audit Course – I: English For Research Paper Writing	Audit Course – I: English For Research Paper Writing	-
24.02.2020 (Monday)	Data and Computer Communication Networks	Analog IC Design	Advanced Computer Methods in Power Systems	Computer Integrated Design and Manufacturing	-
26.02.2020 (Wednesday)	Audio and Speech Signal Processing	Advanced Computer Organization	Application of Power Electronics to Power Systems	Mechanical Vibrations	-
28.02.2020 (Friday)	-	-	High Voltage DC Transmission	Flexible Manufacturing Systems	-
02.03.2020 (Monday)	-	-	Power System Stability	Finite Element Techniques	-

Note: Clash / Omission(s) if any, may be immediately brought to the notice of the undersigned

  
Controller of Examinations

Copy to:

1. The Principal, for kind information.
2. The Director, Admissions and Examinations.
3. The Head, Department of CSE/ECE/EEE/Mech. Engg./H&SS.
4. The Librarian, for information.

} with a request to bring it to the notice of all concerned.