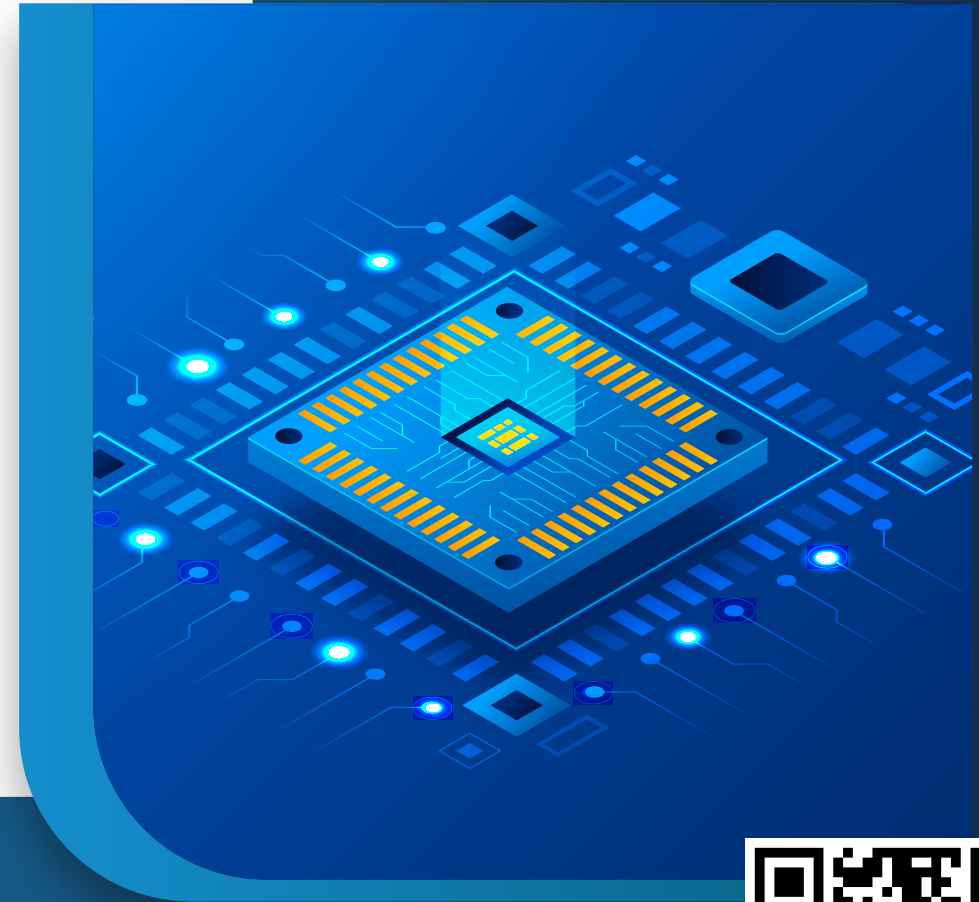


MEMS CMOS INTEGRATION

A Blended Training Program endorsed
by ISSS using Intellisuite and Cadence
Design Flow



IN ASSOCIATION WITH



Vellore Institute of
Technology
Chennai



Electronics and Communication
Department
Chandigarh University



Vasavi College of Engineering
Hyderabad



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ABOUT THE WORKSHOP

The integration of MEMS with CMOS is a dynamic, multidisciplinary research area spanning Electronics, Electrical, Mechanical Engineering and Material Science. For effective signal processing, a read-out Integrated Circuit (ROIC) or Application-Specific Integrated Chip (ASIC) is crucial, posing challenges in adherence to aspects like design rules (DRC) and process design kits (PDK). This workshop provides a unique opportunity to explore sensor integration with IC using Cadence Virtuoso. Participants will have a hands on experience in integrating MEMS design model from Intellisuite with Cadence Virtuoso.

TOPICS



MEMS design and simulation using Intellisuite

Online



Applications of CMOS-MEMS Devices

Online



Fabrication Aspects of CMOS & MEMS

Online



Integration of CMOS and MEMS

Offline

RESOURCE PERSONS

- Dr Ananiah Durai. S – Professor & Director, CNVD, VIT Chennai
- Dr Manikandan. P, Asso. Prof. CNVD, VIT Chennai

- Dr. Sripada Raja – IntelliSense
- Venugopal D Kulkarni – Entuple Technologies
- Engineers from Entuple Technologies

Agenda & Schedule

DAYS	SESSION	TOPIC
Dec 14	10 AM – 10:30 AM Online	Keynote
	10:30 AM – 12 PM Online	MEMS design and simulation – I
	2 PM – 4 PM Online	CMOS Pre-conditioning circuit design techniques
Dec 15	10 AM – 12 PM Online	MEMS design and simulation – II
	2 PM – 4 PM Online	Necessity and the right choice of a drive stage
Dec 18	10 AM – 1 PM Offline	Demo on MEMS sensor design technique and export of CAD exchangeable HDL file
	2 PM – 4 PM Offline	Integrated design and simulation of MEMS Gyroscope with basic CMOS amplifier as readout
Dec 19	10 AM – 1 PM Offline	Integrated design and simulation of MEMS Capacitive Pressure Sensor (CPS) with Folded Cascode amplifier as readout
	2 PM – 4 PM Offline	Integrated design and simulation of MEMS CPS with Multistage CMOS readout
Dec 20	10 AM – 1 PM Offline	Integrated design and simulation of MEMS CPS with Multistage CMOS readout
	2 PM – 4 PM Offline	Alternatives design challenges of CMOS Interface Electronics with desired design target

• Fees: ₹ 4,000/- including GST

• Last Date of Registration: 13/12/2023

OFFLINE/HANDS-ON SESSIONS @

- **Vellore Institute of Technology Chennai**
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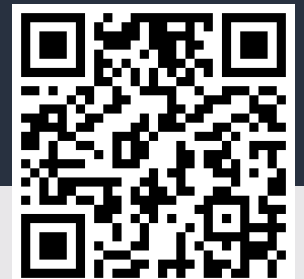
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FOR MORE QUERIES

- Contact No. – 9513400942
- Email – divyavani.sm@entuple.com
- Registration Link – <https://www.abhiyantha.com/mems-cmos-workshop/>