VASAVI COLLEGE OF ENGINEERING (Autonomous)

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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Date: 02.06.2025

Minutes of the 16th meeting of Board of Studies in Electronics and Communication Engineering held on 31.5.2025 (Saturday) at 10.30 a.m.

Members Present:

1.	Dr. E. Sreenivasa Rao Professor & Head	:	Chairman, BoS
2.	Dr. Rajendra Naik Professor, Osmania University, Hyderabad.	:	Osmania University Nominee & Member
3.	Dr. Sreehari Rao Patri Professor, Department of ECE, NIT-Warangal.	:	Member
4.	Dr. Sandeep Chaturvedi, Deputy General Manager, Head MMIC, GAETEC-DRDO, Hyderabad.	:	Member
5.	Sri. J. Sunil, CEO, Vaaluka Solutions Pvt. Ltd., Hyderabad.	:	Member
6.	Sri G. Vinay Kumar GM-Technical, Diqiloqic Systems Pvt. Ltd., Hyderabad.	:	Member
7.	Ms. Cherukuri Phani Madhuri Dy. Manager, IE, Bharat Electronics Ltd., Hyderabad.	:	Member
8.	Sri. Anand S. Moghe Formerly Vice-President (Engineering), M/s. Qualcore Logic Limited.	:	Special Invitee
9.	Dr. K. Veera Swamy, Professor, Dept of ECE, VCE.	:	Member
10.	Ms. G.R. Padmini, Associate Professor, Dept of ECE, VCE.	:	Member
11.	Dr. Srilakshmi Aouthu, Associate Professor, Dept of ECE, VCE.	:	Member
12.	Dr. D.M.K. Chaitanya, Associate Professor, Dept of ECE, VCE.	:	Member
13.	Dr. K. Deepti, Associate Professor, Dept of ECE, VCE.	:	Member
14.	Dr. P. Sathish, Associate Professor, Dept of ECE, VCE.	:	Member
15.	Mr. M. Prasanth, Assistant Professor, Dept of ECE, VCE.	:	Member
16.	Dr. R. Leelavathi, Assistant Professor, Dept of ECE, VCE.	:	Member
17.	Mrs. V. Aruna, Assistant Professor, Dept of ECE, VCE.	:	Member
18.	Dr. S. Aruna Deepthi, Assistant Professor, Dept of ECE, VCE.	:	Member
19.	Mr. V. Krishna Mohan, Assistant Professor, Dept of ECE, VCE.	:	Member
20.	Dr. Vibha D.K, Assistant Professor, Dept of ECE, VCE.	:	Member
21.	Mrs. Ch. Neetu, Assistant Professor, Dept of ECE, VCE.	:	Member
22.	Dr. N. Abid Ali Khan, Assistant Professor, Dept of ECE, VCE.	:	Member
23.	Mr. K. Rama Krishna Reddy, Assistant Professor, Dept of ECE, VCE.	:	Member
24.	Mr. M. Ramanjaneyulu, Assistant Professor, Dept of ECE, VCE.	:	Member
25.	Mr. B. Uma Mahesh Babu, Assistant Professor, Dept of ECE, VCE.	:	Member
26.	Mr. Punna Bharghava, Assistant Professor, Dept of ECE, VCE.	:	Member

The following members of Board of Studies could not attend the meeting.

1. Dr. Zafar Ali Khan Mohammed, Professor, Department of ECE, IIT-Hyderabad.
2. Sri. D.V.R. Murthy, Vice President Strategic Programs, MosChip Technologies Limited.
3. Dr. Srinivasa Rao Zinka, DGM Communication Systems, HBL Power Systems Ltd., Secunderabad.
4. Sri Siva Kumar Mamidisetti, Signal processing Algorithm Lead, Unistring Tech Solutions Pvt. Ltd., Hyd.
5. Dr. Arun Kumar, Professor, Dept of ECE, VCE.
6. Mrs. K.R. Deepthi, Assistant Professor, Dept of ECE, VCE.
7. Dr. Ramya R, Assistant Professor, Dept of ECE, VCE.
8. Dr. M. Praveen Kumar, Assistant Professor, Dept of ECE, VCE.
9. Mr. K. Srikanth, Assistant Professor, Dept of ECE, VCE.
10. Mr. G. Srinivas Rao, Assistant Professor, Dept of ECE, VCE.
11. Mrs. S. Jhansi Naga Laxmi, Assistant Professor, Dept of ECE, VCE.
12. Mr. H. Shravan Kumar, Assistant Professor, Dept of ECE, VCE.
13. Mr. K. Ramakrishna, Assistant Professor, Dept of ECE, VCE.
14. Mr. B. Srinivas Rao, Assistant Professor, Dept of ECE, VCE.
15. Mr. Hafizuddin Mahmad, Assistant Professor, Dept of ECE, VCE.
16. Ms. ABNP Sirisha, Assistant Professor, Dept of ECE, VCE.

Dr. E. Sreenivasa Rao, Chairman BoS presided over the meeting. The following agenda items were taken up for discussion.

1. **Welcoming the members.**
   Chairman welcomed the members to the 16th BoS meeting.
2. **Confirmation of the minutes of 15th BoS meeting held on 18.05.2024.**
   The minutes of the 15th meeting of the Board of Studies held on 18.05.2024 were read and confirmed.
3. **Approval of the action taken report on the resolutions of the 15th BoS meeting held on 18.05.2024**
   The following action taken report was reviewed and approved by the members:

Suggestions offered by the BoS member(s)	Action Taken
Dr. B. Rajendra Naik, Professor, ECE Dept, OU-Hyd. • He enquired about the details of Department level alumni activities.	Following is the brief report on department level alumni activities organized and financial support offered by the ECE Alumni for the A.Y 2024-25: • 10 company specific interaction sessions were conducted to the present final year students during recruitment process. • 8 Alumni activities were conducted on "Career Guidance and Industry Trends". • Dr. K. Venkata Murali, 1982-86 Batch Alumni, Sponsored Rs. 10,000/- to Mr. Karthik, VI-Semester student on Merit-cum-Means. • Mr. Gopu Krishna 1982-86 Batch Alumni, Sponsored Rs. 10,000/- to VI-Semester student on Merit-cum-Means.
Dr. Sreehari Rao Patri Professor, Department of ECE, NIT-Warangal. • He also suggested to shift the VLSI Design	• VLSI Design theory and Lab courses are proposed in VI semester for R-24 & R-25 Regulations.

theory and lab course from VII semester to V / VI semester.	
Sri. D.V.R. Murthy Vice President Strategic Programs, MosChip Technologies Limited. • He advised to start VLSI Design Club in ECE Department and arrange interaction / motivation sessions for junior students with seniors / alumni / working professionals on regular basis.	Department started domain specific clubs namely Embedded & IoT Club, Coding Club, HAM Radio Club, AI Club, VLSI Design Club and organized several activities. Following are the activities conducted under VLSI Design Club. • Hands on session on "Project Ideas and implementation in VLSI Design and Technology" by Ms. Kondaparti Nikhitha, Physical Design Engineer, Moschip Technology, Hyderabad on 15th June 2024 • "Job Opportunities and skills required to get into VLSI Companies" on 28.09.2024 by Pavan Kalyan Tadaka, Analog Design Engineer, MosChip, Hyderabad. • A session on VLSI Design of Operational Transconductance amplifier with specifications was conducted to present VII semester students by Krishna Chaitanya, Analog Design Engineer, MosChip, Hyderabad. • Session on placement opportunities at Moschip was conducted on 20.07.2024 by T. Pavan Kalyan, C. Krishna Chaitanya & M. Siva Prasad, Analog Design Engineer, MosChip, Hyderabad.
Dr. Srinivasa Rao Zinka DGM Communication Systems, HBL Power Systems Ltd., Secunderabad. • He suggested to introduce SDR based experiments in Analog and Digital Communication Lab.	Following new experiments were introduced in Analog & Digital Communication Lab and Wireless Communications professional elective Lab. • Wireless transmission and reception of signals using SDR platform. • Analysing the effects of phase noise, channel noise in higher order modulation using GNU radio. • 2X2 MIMO based Wireless communication experiment using SDR platform.
Sri G. Vinay Kumar GM-Technical, Diqiloqic Systems Pvt. Ltd., Hyderabad. • He suggested to introduce hardware based experiments using test and measurement equipment in Signals and Systems Lab apart from simulation based experiments.	Following New / Additional experiments are introduced in Signals and Systems Lab, Microwave Engineering Lab • Spectrum analysis using Vector Network Analyser (VNA). • Generation of signal using Vector Signal Generator (VSG). • Spectrum analysis using Network Analyser. • Gain and Radiation Pattern analysis and measurement of user defined Antenna. • Measurement of S-parameters using vector network analyser / spectrum analyzer.
Ms. Cherukuri Phani Madhuri Dy. Manager, IE, Bharat Electronics Ltd., Hyderabad. She suggested to introduce constraints based design and analysis experiments in labs.	Course based projects were introduced in R-25 scheme for the following labs to offer constraints based design and analysis problem statements. • Electronics Devices Lab (Synopsys Sentaurus TCAD Device Modelling and

	Simulation Tools). • Electronic Circuits Lab (EDA tools).
Sri Siva Kumar Mamidisetti Signal processing Algorithm Lead, Unistring Tech Solutions Pvt. Ltd., Hyd. • He suggested to introduce hardware based end-to-end DSP based experiments for complete understanding of the system. • Chairman requested his support in this regard and he said he will help faculty in setting up the experiments during Saturdays.	Following operations/calculations are added additionally in the existing experiments. 1. Basic Operations on Frequency spectrum analysis, in-phase and out of phase analysis. 2. Perform SNR calculation on FIR filter 3. Perform SNR calculation on IIR filter 4. Multirate Signal Processing with 3/5, 7/11 I/D ratios.

The chairman BoS, has informed the members that the department has implemented the above suggestions given by the members.

4. Note on the department achievements.

Chairman has presented faculty, students and department achievements for the Academic Year 2024-25 to the members.

To review and recommend the Scheme of Instruction & Examination and Syllabi for all the semester of B.E (ECE), B.E (ECE) Honours and M.E (ES&VLSI Design) Programmes for the academic year 2025-26.

The Chairman has presented Scheme of Instruction & Examination as per the following agenda points to all the members.

5. a. Scheme of instruction & examination for B.E (ECE) I to VIII Semesters (R-25).
    b. Scheme of instruction & examination for B.E (ECE) Honours Degree Program in System on Chip Design.
    c. Syllabi for I and II semester courses (R-25).

6. Scheme of instruction & examination and syllabi for B.E (ECE) III & IV Semesters (R-24).

7. Scheme of instruction & examination and syllabi for B.E (ECE) V & VI Semesters (R-23).

8. Scheme of instruction & examination and syllabi for B.E (ECE) VII & VIII Semesters (R-22).

9. a. Scheme of instruction & examination for M.E (ES&VLSID) I to IV Semesters (R-25).

    b. Scheme of instruction & examination and syllabi for M.E (ES&VLSID) III & IV Semesters (R-24).

10. a. To review and finalize the stream based Open Electives to be offered to B.E (ECE) III to VI Semesters students for the academic year 2025-26.
    b. To review and finalize the stream based Professional Electives to be offered to B.E (ECE) VII semester (PE-I & PE-II Theory and Lab courses) & VIII Semester students for the academic year 2025-26.
    c. To review and finalize the Open Electives to be offered to M.E (ES&VLSID) students for the academic year 2025-26.
    d. To review and finalize the stream based Professional Electives to be offered to M.E (ES&VLSID) students for the academic year 2025-26.

After the deliberations, the members offered the following suggestions.

Name of the BoS member(s)	Suggestions offered
Prof. B. Rajendra Naik, Professor, Department of ECE, O.U, Hyderabad.	• Enquired about fee structure and facilities for B.E. (Hons) Program and reduction of credits for Physics and Chemistry courses. Chairman informed that no additional fees for opting B.E. (Hons) Program and advanced SoC Design lab facility was established with

Name of the BoS member(s)	Suggestions offered
	latest EDA tools. Physics and Chemistry course syllabus & credits was modified and new courses namely Learning to Learn, Critical Thinking, Design Thinking are introduced in the scheme. • Suggested to rename Open Elective course "Introduction to Communication Engineering".
Prof. Sreehari Patri, Professor, Department of ECE, NIT-Warangal.	• Suggested to introduce one-year internship slot specifically for Honours program students to enhance industry exposure and regular students if possible, because now the industry is looking to offer 1 year internships not 6 months. Further, he suggested to offer NPTEL Equivalent courses in 7th semester to implement 1 year internship program. • Suggested to drop theory courses in the VIII - semester to allow students to fully engage in internships without academic overload. Chairman informed that in VIII – semester, online classwork only on Saturday was already implemented to facilitate the students to carryout internship / project work. • Enquired about admission criteria for B.E. (Hons) program. Suggested to change the minimum pass grade at least to 'C' grade for B.E. (Hons) Program to differentiate from minimum pass grade of regular program. • Appreciated introduction of course based projects in the curriculum. Enquired about assessment and evaluation component for proposed course-based projects. Chairman presented the evaluation criteria for theory based course projects and lab based course projects. • Suggested to rename the Open Elective courses: Introduction to Principles of Communication Engineering and Introduction to Communication Systems. • Suggested to change the title of Advanced System Design of B.E. (Hons) Program as per course contents so that without going through the contents, we can easily make out. • Suggested the department to offer two streams in Professional Electives of M.E. (ES&VLSID) instead of 3 streams as the intake is only 12. Also suggested to offer the Open Elective course on "IC Design".
Dr. Sandeep Chaturvedi, Deputy General Manager, Head MMIC, GAETEC-DRDO, Hyderabad.	• Enquired about placement opportunities relevant to SoC Program. • Suggested to facilitate simulation tools like High-Frequency Structure Simulator (HFSS), PCB fabrication and testing facilities for RF Antennas to support prototyping and hands-on learning for UG/PG projects. • Suggested to introduce case studies on complete RTL to GDS VLSI Design flow for Honours and M.E. programmes. • Suggested to utilize open source software's in course based project like Generic Process Design Kit (GPDK) for IC Design, Innovations for High Performance Microelectronics (IHP-130nm) for Analog, RF and Mixed Signal IC Design, SkyWater Open Source Process Design Kit (Sky-130nm). • Advised to obtain the undertaking forms from the students who are using C2S licensed software's tools from MeitY through VPN to avoid any legal issues.

Name of the BoS member(s)	Suggestions offered
	• Suggested to involve few good students in ongoing Research & Consultancy Projects, thereby exposing them to real-time engineering problems. • Suggested to remove / reduce topics that students can learn independently and Accommodate recent technological advancements in the curriculum. • Suggested Research Methodology and Intellectual Property Rights (IPR) course as an audit course instead of credit based course and Introduce RF Design Course with MMIC topics in M.E. Programme.
Sri. J. Sunil, CEO, Vaaluka Solutions Pvt. Ltd., Hyderabad	• Suggested to provide remote/online access to all software's available in the Department to the students to enhance utilization of the tools in projects and to continue simulations and design work beyond campus hours, because it is not residential campus.
Sri. G. Vinay Kumar, GM-Technical, Diqiloqic Systems Pvt. Ltd., Hyderabad	• Suggested the faculty to demonstrate various electronic components and their applications in BCA Lab in the 1st year itself. • Suggested the department to plan & offer the courses like: Introduction to Drone Electronics, Cognitive Radio Networks, Introduction to Electronic Warfare Systems
Smt. Chanukuri Phani Madhuri, Dy. Manager, IE, Bharat Electronics Ltd., Hyderabad	• Suggested to give more emphasis on practical way of teaching the Antennas and Microwave related topics using modern tools.
Sri. Anand S Moghe, Special Invitee, Formerly Vice- President (Engineering), M/s. Qualcore Logic Limited.	• Suggested to rename the Advanced System Design course in B.E. (Hons) Program.

11. Any other item with the permission of the chair.

- It is resolved that the Chairman and external BoS members attended the meeting will be signing all the approved schemes of UG & PG programmes, indicating the approval of the BoS.
- It is also resolved that the Chairman will be counter signing all the syllabi duly signed by the concerned faculty handling the courses of UG & PG programmes in ECE, indicating the approval of BoS.

The meeting concluded with a vote of thanks to the Chair.

(Dr. E. SREENIVASA RAO)
Chairman, BoS - ECE

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Copy to the Hon'ble CEO.
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