



FACULTY DEVELOPMENT PROGRAMME (FDP) ON

“Trends in SoC Design & Its Applications”

9th – 14th December 2019

Organised by

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

VASAVI COLLEGE OF ENGINEERING (AUTONOMOUS), HYDERABAD

in association with NIT, WARANGAL.

Under the scheme of Electronics & ICT Academy, Sponsored by Department of Electronics and Information Technology (DeitY), GOI



Preamble:

“Electronics & ICT Academy” was set up at NIT Warangal with financial assistance from DeitY, MCIT, Govt. of India (GoI). The jurisdiction of this academy is Telangana, Andhra Pradesh, Karnataka, Goa and Puducherry states and UT of Andaman & Nicobar Islands. This academy’s role is to offer faculty development programmes in standardized courses and emerging areas of Electronics, Information Communication Technologies; training & consultancy services for Industry; Curriculum development for Industry; Continuing Education Programs (CEP) for working professionals; Advice and support for technical incubation and entrepreneurial activities.

This faculty development programme (FDP) is aims at imparting state of art in SoC technologies. Participants learn the essential concepts of SoC implementation through a practical hands-on approach utilizing industry design automation (EDA) tools.

Major Course Contents:

- **Recent Advances in SOCs**
- **SoC Architecture and Power Management**
- **Hardware and Software Co-design**
- **IP Integrator**
- **Static Timing Analysis**
- **Implementation of Custom IP Core on Hardware**
- **Case Studies**

Structure of the Course:

- **Theory - 16 hours:** The theory for the topics specified in the Course Contents will be covered in 16 hours duration of classroom sessions.
- **Lab - 24 hours:** The participants will be applying the topics learnt by doing exercises.

Faculty conducting this programme:

Academicians in the concerned field from **Reputed Institutes** from **IITs, and NITs** will be dealing the theory sessions and industry experts will handle the hands-on lab sessions.

Eligibility:

The programme is open to the teachers of engineering colleges, working in the above-mentioned states. Industry personnel working in the concerned/allied discipline can also attend.

The FDP fee may be sent either in the form of Demand Draft or can be paid online as given below:

DD Details	Online Transfer Details
Demand Draft (DD) drawn in favor of “ Electronics & ICT Academy NITW ” and payable at any bank in Warangal.	Account Name: Electronics & ICT Academy NITW Account No: 62423775910 IFSC: SBIN0020149

Registration Fee Particulars:

- **Faculty members or Research Scholars : Rs. 2,500/-**
- **Industry participants : Rs.7,500/-**

Travel and Accommodation:

All the selected participants will have to make their own arrangements for their travel and accommodation. No TA will be paid for the participants. Lunch and snacks will be provided.

How to apply:

A filled in form of application in the prescribed format duly signed and sponsored by appropriate authorities (along with demand draft) should reach: **The Head-Dept. of ECE, Vasavi College of Engineering (Autonomous), Hyderabad** by speed-post. It is also mandatory to send scanned application form and demand draft through e-mail to hodece@staff.vce.ac.in as selection will be intimated ONLY through mail.

Selection Criteria:

Selection will be done based on *first-cum-first-serve basis* and the confirmed candidates will be notified immediately. The maximum number of participants will be **50 (fifty)**. Additionally 10 participants from industry can participate. The list of selected participants will be notified in the institute web sites www.vce.ac.in & www.nitw.ac.in and also will be sent to their personal *e-mail* ids. In case a candidate is not selected, the demand draft will be sent back. A test will be conducted at the end of the course. Candidates will be issued certificates on successful completion of the course along with grade. Reservations are followed for selecting candidates as per GOI norms.

Important dates:

- Last date for submission of application : 25/11/2019**
- Selection-list intimation/display before : 30/11/2019**
- Duration of Program : 9/12/2019 to 14/12/2019**

About the Institute:

Founded in 1981 by Vasavi Academy of Education, Vasavi College of Engineering represents a rich tradition of excellence in technologybased education. A premier-league institution among the affiliates of Osmania University, Vasavi College of Engineering owes its vision to Sri Pendekanti Venkata Subbaiah, a veteran statesman of Independent India. Vasavi College of Engineering has been conferred with Autonomous Status by the University Grants Commission (UGC), New Delhi and Osmania University, Hyderabad for a period of six years with effect from the Academic Year 2014-15, for all UG and PG Programmes. The College is implementing Choice Based Credit System (CBCS). Vasavi College of Engineering, in its 38 years of existence, is a reputed institution in the State of Telangana. The college in its pursuit for quality in technical education has earned 3rd ranking in the State. The college offers six UG (Civil, Mechanical, ECE, CSE, EEE and IT) with total sanctioned intake of 780 and five PG programmes [ME (ES&VLSI); ME (ADM); ME (CE&SP), ME(PSPE) and M.Tech (CSE)] with total sanctioned intake of 96. The College is most preferred by the CET qualified high ranking students.



FORMAT OF APPLICATION
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SPONSORSHIP CERTIFICATE

1. Name : _____
2. Designation : _____
3. Institution : _____
4. E-mail : _____
5. Mobile No. : _____
6. DD No. : _____
- Bank : _____ Date: _____
7. Address for Correspondence:
8. Educational Qualifications with specialization:
9. Subjects taught so far:
10. No. of refresher courses/workshops attended:
11. Experience (in years)
 - Teaching : _____
 - Research : _____
 - Industry : _____
12. Do you belong to SC/ST: YES / NO

Dr./Mr./Ms.....

 is an employee of our Institute/Organization and is hereby sponsored to participate in the FDP on **Trends in SOC Design & Its Applications** sponsored by **Electronics & ICT Academy** during **9th - 14th December 2019** at Vasavi College of Engineering (Autonomous), Ibrahimbagh, Hyderabad-500031.

Place: _____
 Date: _____ Signature of Head of Institution (with seal)

Address for Correspondence:

Post your application form with DD to
Dr. E. Sreenivasa Rao,
 Prof. & HoD
 Department of ECE,
 Vasavi College of Engineering (Autonomous)
 Ibrahimbagh, Hyderabad – 500031,
 Telangana, India.
 Mail the scanned copies of filled-in and duly signed application form with DD to **vce.ece.hod@gmail.com**

For more information visit: <http://nitw.ac.in/eict>

For any enquiry contact:

Mobile : 9441029775 / 9440521840
Land line : 040-23146120 / 23146041

Declaration

The information provided is true to the best of my knowledge. If selected, I agree to abide by the rules and regulations of the FDP and shall attend the course for the entire duration. I also undertake the responsibility to inform the Coordinator in case, I am unable to attend the course.

Place: _____
 Date: _____ Signature of the Applicant

Coordinators

Dr. P. Muralidhar (Coordinator)
 Associate Professor, Dept. of ECE
 NIT Warangal -506004. Ph: 8332969359

G.R. Padmini (Coordinator)
 Associate Professor, Dept. of ECE, VCE(A)
 Ph: 9440521840