

VASAVI COLLEGE OF ENGINEERING (AUTONOMOUS)
IBRAHIMBAGH: HYDERABAD-31

Date: 21-11-2025

Time-Table for B.E. V Semester (Main/Suppl.) Practical Examinations
Academic year 2025-2026

CIVIL

Hydraulics & Hydraulic Machinery Lab

DATES	9.00 AM TO 12.00 NOON	12.00 NOON TO 3.00 PM	3.00 PM TO 6.00PM
12.12.2025	A1	A2	A3
13.12.2025	A4	A5	A6

Environmental Engineering Lab

DATES	9.00 AM TO 12.00 NOON	12.00 NOON TO 3.00 PM	3.00 PM TO 6.00PM
10.12.2025	A1	A2	A3
11.12.2025	A4	A5	A6

Concrete Lab

DATES	9.00 AM TO 12.00 NOON	12.00 NOON TO 3.00 PM	3.00 PM TO 6.00PM
12.12.2025	A4	A5	A6
13.12.2025	A1	A2	A3

BATCHES:-

A1→	1602-23-732-001 TO 1602-23-732-010;	10
A2→	1602-23-732-011, 012, 014, 015, 017, 018, 020, 022 TO 024;	10
A3→	1602-23-732-025 TO 028; 030, 032, 033, 038 TO 040;	10
A4→	1602-23-732-043 TO 045, 047 TO 053;	10
A5→	1602-23-732-054 TO 057, 059, 060; 23-22-732-016; 1602-23-732-301 TO 303;	10
A6→	1602-23-732-304 TO 1602-23-732-313; + ASRC	

(*) ASRC: Any Supplementary Registered Candidates


CHIEF SUPERINTENDENT

VASAVI COLLEGE OF ENGINEERING (AUTONOMOUS)
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Date: 21-11-2025

Time-Table for B.E. V Semester (Main/Suppl.) Practical Examinations
Academic year 2025-2026

CSE (SECTION-A)

Computer Networks Lab

DATES	9.40 AM TO 12.40 PM	1.00 PM TO 4.00 PM
08.12.2025	A1	A2

AI & ML Lab

DATES	9.40 AM TO 12.40 PM	1.00 PM TO 4.00 PM
09.12.2025	A1	A2

Full Stack Web Development Lab

DATES	9.40 AM TO 12.40 PM	1.00 PM TO 4.00 PM
10.12.2025	A1	A2

BATCHES: -

A1→	1602-23-733-001 TO 014, 016 TO 1602-23-733-037;	36
A2→	1602-23-733-038 TO 055, 057 TO 060, 062 TO 066; 23-733-301 TO 307;+ ASRC	

(*) ASRC: Any Supplementary Registered Candidates


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Date: 21-11-2025

Time-Table for B.E. V Semester (Main/Suppl.) Practical Examinations
Academic year 2025-2026.

CSE (SECTION-B)

Computer Networks Lab

DATES	9.40 AM TO 12.40 PM	1.00 PM TO 4.00 PM
09.12.2025	B1	B2

AIML Lab

DATES	9.40 AM TO 12.40 PM	1.00 PM TO 4.00 PM
10.12.2025	B1	B2

Full Stack Web Development Lab

DATES	9.40 AM TO 12.40 PM	1.00 PM TO 4.00 PM
08.12.2025	B1	B2

BATCHES: -

B1→	1602-23-733-067 TO 080, 082 TO 092, 094 TO 103, 105;	36
B2→	1602-23-733-106 TO 1602-23-733-130; 1602-23-733-308 TO 314;+ ASRC	

(*) ASRC: Any Supplementary Registered Candidates



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Time-Table for B.E. V Semester (Main/Suppl.) Practical Examinations
Academic year 2025-2026.

CSE (SECTION-C)

Computer Networks Lab

DATES	9.40 AM TO 12.40 PM	1.00 PM TO 4.00 PM
10.12.2025	C1	C2

AIML Lab

DATES	9.40 AM TO 12.40 PM	1.00 PM TO 4.00 PM
08.12.2025	C1	C2

Full Stack web Development Lab

DATES	9.40 AM TO 12.40 PM	1.00 PM TO 4.00 PM
09.12.2025	C1	C2

BATCHES: -

C1→	1602-23-733-131 TO 1602-23-733-166;	36
C2→	1602-23-733-167 TO 1602-23-733-196; 1602-23-733-315 TO 320;+ ASRC	

(*) ASRC: Any Supplementary Registered Candidates



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Date: 21-11-2025

Time-Table for B.E. V Semester (Main/Suppl.) Practical Examinations
Academic year 2025-2026

CSE-AI&ML

Computer Networks Lab

DATES	9.40 AM TO 12.40 PM	1.00 PM TO 4.00 PM
10.12.2025	A1	A2

AIML Lab

DATES	9.40 AM TO 12.40 PM	1.00 PM TO 4.00 PM
09.12.2025	A1	A2

Full Stack web Development Lab

DATES	9.40 AM TO 12.40 PM	1.00 PM TO 4.00 PM
11.12.2025	A1	A2

BATCHES: -

A1→	1602-23-748-001 TO 017, 019 TO 1602-23-748-023, 025 TO 038;	36
A2→	1602-23-748-039 TO 041, 043 TO 064; 1602-23-748-301 TO 306; + ASRC	

(*) ASRC: Any Supplementary Registered Candidates


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**VASAVI COLLEGE OF ENGINEERING (AUTONOMOUS)
IBRAHIMBAGH: HYDERABAD-31.**

Date: 21-11-2025

**Time-Table for B.E. V Semester (Minors) (Main/Suppl.) Practical
Examinations Academic year 2025-2026**

ECE, EEE, MECH & CIVIL

Operating Systems Lab

DATES	9.40 AM TO 12.40 PM	1.00 PM TO 4.00 PM
16.12.2025	A1	A2

BATCHES: -

A1→	1602-23-735-007, 010, 026, 036, 038, 051, 054, 074, 083, 084, 086, 089, 099, 100, 105, 107, 116, 120, 123, 124, 134, 144, 148, 155, 156, 158, 169, 173, 178, 185;	30
A2→	1602-23-735-187, 192, 319; 22-734-003, 008, 012, 015, 018, 021, 025, 029, 034, 046, 047, 304, 305, 306; 23-736-013, 042, 301, 309, 312; 1602-23-732-015, 018, 025, 033, 301, 302, 303, 304; + ASRC	

(*) ASRC: Any Supplementary Registered Candidates



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Academic year 2025-2026

EEE

AC Machines Lab

DATES	9.00 AM TO 12.00 NOON	12.00 NOON TO 3.00 PM	3.00 PM TO 6.00PM
08.12.2025	A1	A2	A3
09.12.2025	A4	A5	A6

Control Systems Lab

DATES	9.00 AM TO 12.00 NOON	12.00 NOON TO 3.00 PM	3.00 PM TO 6.00PM
12.12.2025	A1	A2	A3
13.12.2025	A4	A5	A6

Linear Integrated Circuits & Applications Lab

DATES	9.00 AM TO 12.00 NOON	12.00 NOON TO 3.00 PM	3.00 PM TO 6.00PM
10.12.2025	A4	A5	A6
11.12.2025	A1	A2	A3

BATCHES:-

A1→	1602-23-734-001 TO 1602-23-734-011;	11
A2→	1602-23-734-012 TO 015, 017, 018, 020 TO 024;	11
A3→	1602-23-734-025 TO 1602-23-734-036;	12
A4→	1602-23-734-039 TO 044, 046 TO 050;	11
A5→	1602-23-734-051 TO 054; 23-734-301 TO 307;	11
A6→	1602-23-734-308 TO 1602-23-734-318; + ASRC	

Mini Project

DATES	9.00 AM TO 12.00 NOON	12.00 NOON TO 3.00 PM	3.00 PM TO 6.00PM
10.12.2025	B1 TO B4	B5 TO B7	B8 TO B11
11.12.2025	B12 TO B15	B16 TO B18	B19 TO B21

BATCHES:-

B1→	1602-23-734-001, 002, 003;	B12→	1602-23-735-039, 040, 041;
B2→	1602-23-734-004, 005, 006;	B13→	1602-23-734-042, 043, 044;
B3→	1602-23-734-007, 008, 009;	B14→	1602-23-734-046, 047, 048;
B4→	1602-23-734-010, 011, 012;	B15→	1602-23-734-049, 050, 051;
B5→	1602-23-734-013, 014, 015;	B16→	1602-23-734-052, 053;
B6→	1602-23-734-017, 018, 020, 036;	B17→	1602-23-734-301, 302, 303;
B7→	1602-23-734-021, 022, 023;	B18→	1602-23-734-304, 305, 306;
B8→	1602-23-734-024, 025, 026;	B19→	1602-23-734-307, 308, 309;
B9→	1602-23-734-027, 028, 029;	B20→	1602-23-734-310, 311, 312;
B10→	1602-23-734-030, 031, 032;	B21→	1602-23-734-313, 314, 315;
B11→	1602-23-734-033, 034, 035;	B22→	1602-23-734-316, 317, 318;22-734-054+ASRC

(*) ASRC: Any Supplementary Registered Candidates


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Date: 21-11-2025

Time-Table for B.E V Semester (Main/Suppl.) Practical Examinations
Academic year 2025-2026

ECE (SECTION-A)

Microprocessors & Microcontrollers Lab

DATES	9.40 AM TO 12.40 PM	1.20 PM TO 4.20 PM
12.12.2025	A1 TO A3	A4 TO A6

Integrated Circuits & Applications Lab

DATES	9.00 AM TO 12.00 NOON	12.00 NOON TO 3.00 PM	3.00 PM TO 6.00PM
08.12.2025	A1 & A2	A3 & A4	A5 & A6

Analog & Digital Communication Lab

DATES	9.00 AM TO 12.00 NOON	12.00 NOON TO 3.00 PM	3.00 PM TO 6.00PM
10.12.2025	A1 & A2	A3 & A4	A5 & A6

BATCHES:-

A1→	1602-23-735-001 TO 1602-23-735-012;	12
A2→	1602-23-735-013 TO 1602-23-735-024;	12
A3→	1602-23-735-025 TO 1602-23-735-036;	12
A4→	1602-23-735-037 TO 1602-23-735-048;	12
A5→	1602-23-735-049 TO 1602-23-735-060;	12
A6→	1602-23-735-061 TO 064; 22-735-034; 23-735-301 TO 1602-23-735-307; + ASRC	

(*) ASRC: Any Supplementary Registered Candidates

Mini Project-II

DATES	9.40 AM TO 12.40 PM	1.20 PM TO 4.20 PM
15.12.2025	A TO A11	A12 TO A22

BATCHES:-

A1→	1602-23-735-001, 004, 021;	A14→	1602-23-735-018, 040, 044;
A2→	1602-23-735-064, 302, 005;	A15→	1602-23-735-012, 013, 029;
A3→	1602-23-735-055, 063, 046;	A16→	1602-23-735-007, 019, 042;
A4→	1602-23-735-0015, 009, 008;	A17→	1602-23-735-034, 045, 058;
A5→	1602-23-735-010, 036, 038;	A18→	1602-23-735-028, 031, 060;
A6→	1602-23-735-006, 051;	A19→	1602-23-735-014, 027, 035;
A7→	1602-23-735-002, 059;	A20→	1602-23-735-301, 304, 305;
A8→	1602-23-735-022, 023, 024;	A21→	1602-23-735-048, 050, 056;
A9→	1602-23-735-011, 037, 062;	A22→	1602-23-735-303, 306, 307;
A10→	1602-23-735-053, 052, 017;	A23→	1602-23-735-043, 047;
A11→	1602-23-735-039, 054, 061;	A24→	1602-23-735-033, 041, 057;
A12→	1602-23-735-026, 030, 049;	A25→	1602-23-735-003, 025, 032;+ ASRC
A13→	1602-23-735-016, 020, 22-735-034;		

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Time-Table for B.E V Semester (Main/Suppl.) Practical Examinations
Academic year 2025-2026

ECE (SECTION-B)

Microprocessors & Microcontrollers Lab

DATES	9.40 AM TO 12.40 PM	1.20 PM TO 4.20 PM
10.12.2025	B1 TO B3	B4 TO B6

Integrated Circuits & Applications Lab

DATES	9.00 AM TO 12.00 NOON	12.00 NOON TO 3.00 PM	3.00 PM TO 6.00PM
09.12.2025	B1 & B2	B3 & B4	B5 & B6

Analog & Digital Communication Lab

DATES	9.00 AM TO 12.00 NOON	12.00 NOON TO 3.00 PM	3.00 PM TO 6.00PM
08.12.2025	B1 & B2	B3 & B4	B5 & B6

BATCHES:-

B1→	1602-23-735-065 TO 070, 072 TO 074, 076 TO 078;	12
B2→	1602-23-735-079 TO 090;	12
B3→	1602-23-735-091 TO 093, 095 TO 103;	12
B4→	1602-23-735-104 TO 1602-23-735-115;	12
B5→	1602-23-735-116 TO 124, 126 TO 128;	12
B6→	1602-22-735-087, 1602-23-735-308 TO 1602-23-735-312; + ASRC	

(*) ASRC: Any Supplementary Registered Candidates

Mini Project-II

DATES	9.40 AM TO 12.40 PM	1.20 PM TO 4.20 PM
11.12.2025	B1 TO B11	B12 TO B22

BATCHES:-

B1→	1602-23-735-070, 077, 078;	B12→	1602-23-735-074, 079, 085;
B2→	1602-23-735-068, 082, 088;	B13→	1602-23-735-084, 124, 108;
B3→	1602-23-735-112, 113, 122;	B14→	1602-23-735-104, 105, 115;
B4→	1602-23-735-066, 091, 126;	B15→	1602-23-735-116, 106, 086;
B5→	1602-23-735-308, 310, 312, 072;	B16→	1602-23-735-076, 311, 309;
B6→	1602-23-735-087, 098, 120;	B17→	1602-23-735-092, 119, 093;
B7→	1602-23-735-109, 111, 110, 128;	B18→	1602-23-735-096, 102, 107;
B8→	1602-23-735-089, 095, 127;	B19→	1602-23-735-083, 100, 123;
B9→	1602-23-735-099, 081;	B20→	1602-23-735-073, 080, 114, 117;
B10→	1602-23-735-065, 069, 090;	B21→	1602-23-735-067; 22-735-087, 113;
B11→	1602-23-735-103, 118, 121;	B22→	1602-23-735-101, 097; + ASRC

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Date: 21-11-2025

Time-Table for B.E V Semester (Main/Suppl.) Practical Examinations
Academic year 2025-2026

ECE (SECTION-C)

Microprocessors & Microcontrollers Lab

DATES	9.40 AM TO 12.40 PM	1.20 PM TO 4.20 PM
08.12.2025	C1 TO C3	C4 TO C6

Integrated Circuits & Applications Lab

DATES	9.00 AM TO 12.00 NOON	12.00 NOON TO 3.00 PM	3.00 PM TO 6.00PM
15.12.2025	C1 & C2	C3 & C4	C5 & C6

Analog & Digital Communication Lab

DATES	9.00 AM TO 12.00 NOON	12.00 NOON TO 3.00 PM	3.00 PM TO 6.00PM
12.12.2025	C1 & C2	C3 & C4	C5 & C6

BATCHES:-

C1→	1602-23-735-129 TO 134, 136 TO 141;	12
C2→	1602-23-735-142 TO 1602-23-735-153;	12
C3→	1602-23-735-154 TO 164, 166;	12
C4→	1602-23-735-167 TO 1602-23-735-178;	12
C5→	1602-23-735-179 TO 1602-23-735-190;	12
C6→	1602-23-735-191 TO 193; 22-735-113; 23-735-315 TO 1602-23-735-320; + ASRC	

(*) ASRC: Any Supplementary Registered Candidates

Mini Project-II

DATES	9.40 AM TO 12.40 PM	1.20 PM TO 4.20 PM
10.12.2025	C1 & C12	C13 & C23

BATCHES:-

C1→	1602-23-735-157, 160, 164;	C14→	1602-23-735-154, 190, 134;
C2→	1602-23-735-178, 179;	C15→	1602-23-735-138, 139, 171;
C3→	1602-23-735-147, 175, 176;	C16→	1602-23-735-140, 177, 315;
C4→	1602-23-735-136, 161, 189;	C17→	1602-23-735-137, 316, 317;
C5→	1602-23-735-148, 185, 192;	C18→	1602-23-735-142, 143, 150;
C6→	1602-23-735-153, 184, 319;	C19→	1602-23-735-149, 151, 180;
C7→	1602-23-735-187, 129;	C20→	1602-23-735-131, 145, 163;
C8→	1602-23-735-130, 173, 188;	C21→	1602-23-735-162, 168, 181;
C9→	1602-23-735-193, 318, 320;	C22→	1602-23-735-174, 182, 183;
C10→	1602-23-735-166, 169, 172;	C23→	1602-23-735-152, 158, 191;
C11→	1602-23-735-144, 155, 156;	C24→	1602-23-735-141, 186; + ASRC
C12→	1602-23-735-159, 167, 170;		
C13→	1602-23-735-132, 133, 146;		

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VASAVI COLLEGE OF ENGINEERING (AUTONOMOUS)
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Date: 21-11-2025

Time-Table for (B.E Honours) V-Semester Practical Examinations for the
Academic year 2025-2026.

ECE (SECTION-A, B & C)

FPGA Based System Design Lab

DATES	9.40 AM TO 12.40 PM	1.20 PM TO 4.20 PM
13.12.2025	P1 TO P25	P26 TO P49

BATCHES:-

P1→	1602-23-735-001	P26→	1602-23-735-128
P2→	1602-23-735-013	P27→	1602-23-735-311
P3→	1602-23-735-017	P28→	1602-23-735-130
P4→	1602-23-735-018	P29→	1602-23-735-132
P5→	1602-23-735-031	P30→	1602-23-735-142
P6→	1602-23-735-034	P31→	1602-23-735-152
P7→	1602-23-735-042	P32→	1602-23-735-153
P8→	1602-23-735-045	P33→	1602-23-735-154
P9→	1602-23-735-049	P34→	1602-23-735-157
P10→	1602-23-735-053	P35→	1602-23-735-160
P11→	1602-23-735-058	P36→	1602-23-735-161
P12→	1602-23-735-302	P37→	1602-23-735-163
P13→	1602-23-735-303	P38→	1602-23-735-164
P14→	1602-23-735-306	P39→	1602-23-735-168
P15→	1602-23-735-070	P40→	1602-23-735-170
P16→	1602-23-735-078	P41→	1602-23-735-171
P17→	1602-23-735-085	P42→	1602-23-735-176
P18→	1602-23-735-088	P43→	1602-23-735-179
P19→	1602-23-735-090	P44→	1602-23-735-181
P20→	1602-23-735-092	P45→	1602-23-735-182
P21→	1602-23-735-095	P46→	1602-23-735-183
P22→	1602-23-735-108	P47→	1602-23-735-189
P23→	1602-23-735-117	P48→	1602-23-735-316
P24→	1602-23-735-121	P49→	1602-23-735-317+ ASRC
P25→	1602-23-735-127		

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Time-Table for B.E. V Semester (Main/Suppl.) Practical Examinations
Academic year 2025-2026

MECH

Manufacturing Processes Lab

DATES	9.40 AM TO 12.40 PM	1.20 PM TO 4.20 PM
13.12.2025	A1	A2
15.12.2025	A3	A4

CAD/CAM Lab

DATES	9.40 AM TO 12.40 PM	1.20 PM TO 4.20 PM
13.12.2025	A3	A4
15.12.2025	A1	A2

Programming for Mechanical Engineers Lab

DATES	9.40 AM TO 12.40 PM	1.20 PM TO 4.20 PM
08.12.2025	A1 & A2	A3 & A4

BATCHES:-

A1→	1602-23-736-003 TO 009, 011 TO 013, 015 TO 018, 020;	15
A2→	1602-23-736-021 TO 025, 027 TO 032, 034 TO 037;	15
A3→	1602-23-736-038 TO 046, 048 TO 051, 054; 21-736-010;	15
A4→	1602-22-736-077; 1602-23-736-301 TO 309, 311 TO 315; + ASRC	

(*) ASRC: Any Supplementary Registered Candidates

Mini Project

DATES	9.40 AM TO 12.40 PM	1.20 PM TO 4.20 PM
12.12.2025	P1 TO P10	P11 TO P20

BATCHES:-

P1→	1602-23-736-007, 030, 028;	P11→	1602-23-736-042, 041, 006;
P2→	1602-23-736-013, 005, 020;	P12→	1602-23-736-022, 017, 021;
P3→	1602-23-736-312, 050, 22-736-077;	P13→	1602-23-736-313, 311, 027;
P4→	1602-23-736-003, 024, 029;	P14→	1602-23-736-309, 315, 034;
P5→	1602-23-736-307, 004, 009;	P15→	1602-23-736-040, 023, 018;
P6→	1602-23-736-308, 314, 306;	P16→	1602-23-736-304, 048, 036;
P7→	1602-23-736-032, 025, 008;	P17→	1602-23-736-303, 043, 21-736-010;
P8→	1602-23-736-031, 037, 011;	P18→	1602-23-736-301, 051, 012;
P9→	1602-23-736-045, 046, 044;	P19→	1602-23-736-302, 054, 305;
P10→	1602-23-736-016, 015, 039;	P20→	1602-23-736-038, 035, 049 + ASRC

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Date: 21-11-2025

Time-Table for B.E. V Semester (Main/Suppl.) Practical Examinations
Academic year 2025-2026

I.T (SECTION-A)

Computer Networks Lab

DATES	9.40 AM TO 12.40 PM	1.20 PM TO 4.20 PM
09.12.2025	A1	A2

Artificial Intelligence and Machine Learning Lab

DATES	9.40 AM TO 12.40 PM	1.20 PM TO 4.20 PM
11.12.2025	A1	A2

Microprocessor & Interfacing Lab

DATES	9.40 AM TO 12.40 PM	1.20 PM TO 4.20 PM
10.12.2025	A1	A2

Operating Systems Lab

DATES	9.40 AM TO 12.40 PM	1.20 PM TO 4.20 PM
08.12.2025	A1	A2

BATCHES:-

A1→	1602-23-737-001 TO 010, 012 TO 1602-23-737-035;	34
A2→	1602-23-737-036 TO 046, 048, 049, 051, 053 TO 057, 059 TO 065; 1602-23-737-301 TO 307; +ASRC	

(*) ASRC: Any Supplementary Registered Candidates



CHIEF SUPERINTENDENT

VASAVI COLLEGE OF ENGINEERING (AUTONOMOUS)
IBRAHIMBAGH: HYDERABAD-31

Date: 21-11-2025

Time-Table for B.E. V Semester (Main/Suppl.) Practical Examinations
Academic year 2025-2026

I.T (SECTION-B)

Computer Networks Lab

DATES	9.40 AM TO 12.40 PM	1.20 PM TO 4.20 PM
11.12.2025	B1	B2

Artificial Intelligence and Machine Learning Lab

DATES	9.40 AM TO 12.40 PM	1.20 PM TO 4.20 PM
09.12.2025	B1	B2

Microprocessor & Interfacing Lab

DATES	9.40 AM TO 12.40 PM	1.20 PM TO 4.20 PM
08.12.2025	B1	B2

Operating Systems Lab

DATES	9.40 AM TO 12.40 PM	1.20 PM TO 4.20 PM
12.12.2025	B1	B2

BATCHES:-

B1→	1602-23-737-066 TO 1602-23-737-100;	35
B2→	1602-23-737-101 TO 1602-23-737-129; 1602-23-737-308 TO 313+ASRC	

(*) ASRC: Any Supplementary Registered Candidates



CHIEF SUPERINTENDENT

VASAVI COLLEGE OF ENGINEERING (AUTONOMOUS)
IBRAHIMBAGH: HYDERABAD-31

Date: 21-11-2025

Time-Table for B.E. V Semester (Main/Suppl.) Practical Examinations
Academic year 2025-2026

I.T (SECTION-C)

Computer Networks Lab

DATES	9.40 AM TO 12.40 PM	1.20 PM TO 4.20 PM
09.12.2025	C1	C2

Artificial Intelligence and Machine Learning Lab

DATES	9.40 AM TO 12.40 PM	1.20 PM TO 4.20 PM
11.12.2025	C1	C2

Microprocessor & Interfacing Lab

DATES	9.40 AM TO 12.40 PM	1.20 PM TO 4.20 PM
12.12.2025	C1	C2

Operating Systems Lab

DATES	9.40 AM TO 12.40 PM	1.20 PM TO 4.20 PM
10.12.2025	C1	C2

BATCHES:-

C1→	1602-23-737-130 TO 132, 134 TO 1602-23-737-165;	35
C2→	1602-23-737-166 TO 187, 189 TO 194; 1602-23-737-314 TO 319; +ASRC	

(*) ASRC: Any Supplementary Registered Candidates


CHIEF SUPERINTENDENT

VASAVI COLLEGE OF ENGINEERING (AUTONOMOUS)
IBRAHIMBAGH: HYDERABAD-31.

Date: 21-11-2025

Time-Table for (B.E. Honours in AI&ML) V-Semester (Main/Suppl.)
Practical Examinations for the Academic year 2025-2026.

I.T (SECTION-A, B & C)

Computational Intelligence Lab

DATES	9.40 AM TO 12.40 PM	1.20 PM TO 4.20 PM
13.12.2025	P1 TO P30	P31 TO P59

BATCHES:-

P1→	1602-23-737-003	P31→	1602-23-737-079
P2→	1602-23-737-004	P32→	1602-23-737-081
P3→	1602-23-737-007	P33→	1602-23-737-082
P4→	1602-23-737-009	P34→	1602-23-737-086
P5→	1602-23-737-010	P35→	1602-23-737-090
P6→	1602-23-737-014	P36→	1602-23-737-095
P7→	1602-23-737-015	P37→	1602-23-737-099
P8→	1602-23-737-019	P38→	1602-23-737-108
P9→	1602-23-737-020	P39→	1602-23-737-116
P10→	1602-23-737-021	P40→	1602-23-737-120
P11→	1602-23-737-024	P41→	1602-23-737-122
P12→	1602-23-737-026	P42→	1602-23-737-128
P13→	1602-23-737-027	P43→	1602-23-737-131
P14→	1602-23-737-028	P44→	1602-23-737-132
P15→	1602-23-737-030	P45→	1602-23-737-136
P16→	1602-23-737-031	P46→	1602-23-737-145
P17→	1602-23-737-035	P47→	1602-23-737-146
P18→	1602-23-737-039	P48→	1602-23-737-148
P19→	1602-23-737-041	P49→	1602-23-737-150
P20→	1602-23-737-046	P50→	1602-23-737-160
P21→	1602-23-737-054	P51→	1602-23-737-162
P22→	1602-23-737-060	P52→	1602-23-737-167
P23→	1602-23-737-061	P53→	1602-23-737-178
P24→	1602-23-737-062	P54→	1602-23-737-179
P25→	1602-23-737-063	P55→	1602-23-737-181
P26→	1602-23-737-064	P56→	1602-23-737-182
P27→	1602-23-737-065	P57→	1602-23-737-186
P28→	1602-23-737-301	P58→	1602-23-737-187
P29→	1602-23-737-305	P59→	1602-23-737-189+ ASRC
P30→	1602-23-737-076		

(*) ASRC: Any Supplementary Registered Candidates



CHIEF SUPERINTENDENT