VASAVI COLLEGE OF ENGINEERING (AUTONOMOUS)
IBRAHIMBAGH: HYDERABAD-31

Date: 10-11-2026

Time-Table for B.E. V Semester (Main/Suppl.) Practical Examinations
Academic year 2026-2027

CIVIL

Hydraulics & Hydraulic Machinery Lab

DATES	9.40 AM TO 12.40 PM	1.00 PM TO 4.00 PM
04.12.2026	A1	A2
05.12.2026	A3	A4

Environmental Engineering Lab

DATES	9.40 AM TO 12.40 PM	1.00 PM TO 4.00 PM
02.12.2026	A1	A2
03.12.2026	A3	A4

Concrete Lab

DATES	9.40 AM TO 12.40 PM	1.00 PM TO 4.00 PM
04.12.2026	A3	4A
05.12.2026	A1	A2

BATCHES:-

A1→	1602-24-732-001 TO 006, 008 TO 013, 015, 016, 018, 020, 022, 023;	17
A2→	1602-24-732-025 TO 030, 032 TO 036, 039 TO 044;	17
A3→	1602-24-732-046 TO 1602-24-732-062;	17
A4→	1602-24-732-063, 064; 1602-24-732-301 TO 311; 1602-23-732-013, 019, 031; + ASRC	

(*) ASRC: Any Supplementary Registered Candidates

CHIEF SUPERINTENDENT

VASAVI COLLEGE OF ENGINEERING (AUTONOMOUS)
IBRAHIMBAGH: HYDERABAD-31

Date: 10-11-2026

Time-Table for B.E. V Semester (Main/Suppl.) Practical Examinations
Academic year 2026-2027

CSE (SECTION-A)

Computer Networks Lab

DATES	9.40 AM TO 12.40 PM	1.00 PM TO 4.00 PM
01.12.2026	A1	A2

AI & ML Lab

DATES	9.40 AM TO 12.40 PM	1.00 PM TO 4.00 PM
02.12.2026	A1	A2

Full Stack Development Lab

DATES	9.40 AM TO 12.40 PM	1.00 PM TO 4.00 PM
03.12.2026	A1	A2

IOT Lab

DATES	9.40 AM TO 12.40 PM	1.00 PM TO 4.00 PM
04.12.2026	A1	A2

BATCHES: -

A1→	1602-24-733-001 TO 011, 013 TO 1602-24-733-037;	36
A2→	1602-24-733-038, 039, 041 TO 065; 1602-24-733-301 TO 307; 1602-23-733-061+ ASRC	

(*) ASRC: Any Supplementary Registered Candidates

Theme Based Project-I

DATES	9.40 AM TO 12.40 PM	1.00 PM TO 4.00 PM
05.12.2026	A1 TO A12	A13 TO A23

BATCHES:-

A1→	1602-24-733-046, 051, 008;	A13→	1602-24-733-019, 023, 029;
A2→	1602-24-733-002, 027, 053;	A14→	1602-24-733-004, 307, 020;
A3→	1602-24-733-014, 006, 062;	A15→	1602-24-733-050, 049, 041;
A4→	1602-24-733-009, 034, 039;	A16→	1602-24-733-032, 022, 042;
A5→	1602-24-733-044, 007, 047;	A17→	1602-24-733-054, 048, 018;
A6→	1602-24-733-005, 017, 021;	A18→	1602-24-733-033, 031, 016;
A7→	1602-24-733-001, 024, 055;	A19→	1602-24-733-056, 057, 059;
A8→	1602-24-733-011, 038, 060;	A20→	1602-24-733-301, 305, 306;
A9→	1602-24-733-003, 063, 064;	A21→	1602-24-733-013, 045, 065;
A10→	1602-24-733-035, 058, 061;	A22→	1602-24-733-302, 303, 304;
A11→	1602-24-733-010, 036, 037;	A23→	1602-24-733-015, 028, 043; + ASRC
A12→	1602-24-733-025, 026, 052;		

(*) ASRC: Any Supplementary Registered Candidates

CHIEF SUPERINTENDENT

VASAVI COLLEGE OF ENGINEERING (AUTONOMOUS)
IBRAHIMBAGH: HYDERABAD-31

Date: 10-11-2026

Time-Table for B.E. V Semester (Main/Suppl.) Practical Examinations
Academic year 2026-2027.

CSE (SECTION-B)

Computer Networks Lab

DATES	9.40 AM TO 12.40 PM	1.00 PM TO 4.00 PM
02.12.2026	B1	B2

AI & ML Lab

DATES	9.40 AM TO 12.40 PM	1.00 PM TO 4.00 PM
01.12.2026	B1	B2

Full Stack Development Lab

DATES	9.40 AM TO 12.40 PM	1.00 PM TO 4.00 PM
04.12.2026	B1	B2

IOT Lab

DATES	9.40 AM TO 12.40 PM	1.00 PM TO 4.00 PM
03.12.2026	B1	B2

BATCHES: -

B1→	1602-24-733-066 TO 079, 081 TO 1602-24-733-100;	34
B2→	1602-24-733-102 TO 105, 107 TO 112, 114 TO 130; 1602-24-733-308 TO 314;+ ASRC	

(*) ASRC: Any Supplementary Registered Candidates

Theme Based Project-I

DATES	9.40 AM TO 12.40 PM	1.00 PM TO 4.00 PM
05.12.2026	B1 TO B12	B13 TO B23

BATCHES:-

B1→	1602-24-733-090, 310, 314;	B13→	1602-24-733-103, 104, 124;
B2→	1602-24-733-070, 091, 139;	B14→	1602-24-733-311, 084, 110;
B3→	1602-24-733-066, 068, 079;	B15→	1602-24-733-078, 097, 107;
B4→	1602-24-733-128, 129, 309;	B16→	1602-24-733-112, 114, 116;
B5→	1602-24-733-077, 099, 100;	B17→	1602-24-733-074, 118, 127;
B6→	1602-24-733-067, 121, 308;	B18→	1602-24-733-083, 089, 095;
B7→	1602-24-733-069, 096;	B19→	1602-24-733-088, 087, 071;
B8→	1602-24-733-082, 094, 108;	B20→	1602-24-733-072, 111, 115;
B9→	1602-24-733-085, 119, 122;	B21→	1602-24-733-102, 105, 126;
B10→	1602-24-733-092, 093, 312;	B22→	1602-24-733-098, 109, 123;
B11→	1602-24-733-086, 076, 073;	B23→	1602-24-733-075, 125; + ASRC
B12→	1602-24-733-081, 117, 120;		

(*) ASRC: Any Supplementary Registered Candidates

CHIEF SUPERINTENDENT

VASAVI COLLEGE OF ENGINEERING (AUTONOMOUS)
IBRAHIMBAGH: HYDERABAD-31

Date: 10-11-2026

Time-Table for B.E. V Semester (Main/Suppl.) Practical Examinations
Academic year 2026-2027.

CSE (SECTION-C)

Computer Networks Lab

DATES	9.40 AM TO 12.40 PM	1.00 PM TO 4.00 PM
03.12.2026	C1	C2

AI & ML Lab

DATES	9.40 AM TO 12.40 PM	1.00 PM TO 4.00 PM
04.12.2026	C1	C2

Full Stack Development Lab

DATES	9.40 AM TO 12.40 PM	1.00 PM TO 4.00 PM
01.12.2026	C1	C2

IOT Lab

DATES	9.40 AM TO 12.40 PM	1.00 PM TO 4.00 PM
02.12.2026	C1	C2

BATCHES: -

C1→	1602-24-733-131 TO 154, 156 TO 160, 162 TO 1602-24-733-165;	33
C2→	1602-24-733-166 TO 180, 182 TO 187, 189 TO 194; 1602-24-733-315 TO 320;+ ASRC	

(*) ASRC: Any Supplementary Registered Candidates

Theme Based Project-I

DATES	9.40 AM TO 12.40 PM	1.00 PM TO 4.00 PM
05.12.2026	C1 TO C12	C13 TO C23

BATCHES:-

C1→	1602-24-733-139, 185, 187;	C13→	1602-24-733-169, 179, 189;
C2→	1602-24-733-167, 191, 137;	C14→	1602-24-733-170, 183, 147;
C3→	1602-24-733-138, 157, 182;	C15→	1602-24-733-175, 151, 142;
C4→	1602-24-733-168, 173, 174;	C16→	1602-24-733-315, 320, 171;
C5→	1602-24-733-152, 131, 163;	C17→	1602-24-733-319, 316, 317;
C6→	1602-24-733-144, 172, 190;	C18→	1602-24-733-149, 165, 176;
C7→	1602-24-733-135, 145, 186;	C19→	1602-24-733-136, 159, 146;
C8→	1602-24-733-194, 134, 156;	C20→	1602-24-733-177, 141, 132;
C9→	1602-24-733-140, 143, 193;	C21→	1602-24-733-177, 141, 132;
C10→	1602-24-733-160, 162, 158;	C22→	1602-24-733-166, 178, 318;
C11→	1602-24-733-153, 154, 180;	C23→	1602-24-733-184, 192, 133; + ASRC
C12→	1602-24-733-148, 150, 164;		

(*) ASRC: Any Supplementary Registered Candidates

CHIEF SUPERINTENDENT

VASAVI COLLEGE OF ENGINEERING (AUTONOMOUS)
IBRAHIMBAGH: HYDERABAD-31.

Date: 10-11-2026

Time-Table for B.E. V Semester (Main/Suppl.) Practical Examinations
Academic year 2026-2027

CSE–AI&ML

Computer Networks Lab

DATES	9.40 AM TO 12.40 PM	1.00 PM TO 4.00 PM
04.12.2026	D1	D2

AI & ML Lab

DATES	9.40 AM TO 12.40 PM	1.00 PM TO 4.00 PM
03.12.2026	D1	D2

Full Stack Development Lab

DATES	9.40 AM TO 12.40 PM	1.00 PM TO 4.00 PM
02.12.2026	D1	D2

IOT Lab

DATES	9.40 AM TO 12.40 PM	1.00 PM TO 4.00 PM
01.12.2026	D1	D2

BATCHES: -

D1→	1602-24-748-001 TO 005, 007 TO 015, 017 TO 1602-24-748-037;	35
D2→	1602-24-748-038 TO 064, 1602-24-748-301 TO 307; 1602-23-748-018; + ASRC	

(*) ASRC: Any Supplementary Registered Candidates

Theme Based Project-I

DATES	9.40 AM TO 12.40 PM	1.00 PM TO 4.00 PM
05.12.2026	D1 TO D12	D13 TO D24

BATCHES:-

D1→	1602-24-748-030, 032, 002;	D13→	1602-24-748-052, 063, 024;
D2→	1602-24-748-302, 305, 307;	D14→	1602-24-748-025, 003, 061;
D3→	1602-24-748-009, 021, 062;	D15→	1602-24-748-001, 011, 058;
D4→	1602-24-748-047, 035, 038;	D16→	1602-24-748-008, 012, 064;
D5→	1602-24-748-057, 060, 056;	D17→	1602-24-748-050, 036, 033;
D6→	1602-24-748-048, 059, 045;	D18→	1602-24-748-028, 039, 046;
D7→	1602-24-748-007, 017, 018;	D19→	1602-24-748-022, 301, 306;
D8→	1602-24-748-049, 303, 304;	D20→	1602-24-748-023, 014, 027;
D9→	1602-24-748-020, 043;	D21→	1602-24-748-005, 034, 051;
D10→	1602-24-748-044, 018, 029;	D22→	1602-24-748-037, 055, 013;
D11→	1602-24-748-053, 031, 019;	D23→	1602-24-748-026, 015;
D12→	1602-24-748-004, 054, 040;	D24→	1602-24-748-042, 041, 010; + asrc

(*) ASRC: Any Supplementary Registered Candidates

CHIEF SUPERINTENDENT

VASAVI COLLEGE OF ENGINEERING (AUTONOMOUS)
IBRAHIMBAGH: HYDERABAD-31

Date: 10-11-2026

Time-Table for B.E. V Semester (Minor Degree) (Main/Suppl.) Practical Examinations Academic year 2026-2027

CIVIL, ECE, EEE & MECH

Operating Systems Lab

DATES	9.40 AM TO 12.40 PM	1.00 PM TO 4.00 PM
05.12.2026	A1	A2

BATCHES:-

A1→	1602-24-735-014, 31, 35, 052, 054, 060, 095, 125, 135, 142, 162, 171, 175, 177, 180, 184, 314; 1602-24-734-001, 004, 025, 026, 027;	22
A2→	1602-24-734-034, 036, 041, 050, 051, 052, 054, 058, 059, 061, 303; 1602-24-736-027, 033, 034, 038, 058; 1602-24-732-016, 026, 040, 301; +ASRC	

(*) ASRC: Any Supplementary Registered Candidates

CHIEF SUPERINTENDENT

VASAVI COLLEGE OF ENGINEERING (AUTONOMOUS)
IBRAHIMBAGH: HYDERABAD-31.

Date:10-11-2026

Time-Table for B.E. V Semester (Main/Suppl.) Practical Examinations
Academic year 2026-2027

EEE

Power Electronics Lab

DATES	9.00 AM TO 12.00 NOON	12.00 NOON TO 3.00 PM	3.00 PM TO 6.00PM
30.11.2026	A1	A2	A3
01.12.2026	A4	A5	A6

Linear Integrated Circuits & Applications Lab

DATES	9.00 AM TO 12.00 NOON	12.00 NOON TO 3.00 PM	3.00 PM TO 6.00PM
30.11.2026	A4	A5	A6
01.12.2026	A1	A2	A3

Mini Project

DATES	9.00 AM TO 12.00 NOON	12.00 NOON TO 3.00 PM	3.00 PM TO 6.00PM
02.12.2026	A1 TO A2	A3 TO A4	A5 TO A6

BATCHES:-

A1→	1602-24-734-001 TO 1602-24-734-011;	11
A2→	1602-24-734-012 TO 014, 016 TO 018, 020 TO 1602-24-734-024;	11
A3→	1602-24-734-025 TO 1602-24-734-035;	11
A4→	1602-24-734-036 TO 1602-24-734-046;	11
A5→	1602-24-734-047 TO 052, 054 TO 058;	11
A6→	1602-24-734-059 TO 061; 1602-24-734-301 TO 306; + ASRC	

(*) ASRC: Any Supplementary Registered Candidates

CHIEF SUPERINTENDENT

VASAVI COLLEGE OF ENGINEERING (AUTONOMOUS)
IBRAHIMBAGH: HYDERABAD-31.

Date: 10-11-2026

Time-Table for B.E V Semester (Main/Suppl.) Practical Examinations
Academic year 2026-2027

ECE (SECTION-A)

Microprocessors & Microcontrollers Lab

DATES	9.40 AM TO 12.40 PM	1.20 PM TO 4.20 PM
01.12.2026	A1 TO A3	A4 TO A6

Integrated Circuits & Applications Lab

DATES	9.00 AM TO 12.00 NOON	12.00 NOON TO 3.00 PM	3.00 PM TO 6.00PM
02.12.2026	A1 & A2	A3 & A4	A5 & A6

Analog and Digital Communication Lab

DATES	9.00 AM TO 12.00 NOON	12.00 NOON TO 3.00 PM	3.00 PM TO 6.00PM
30.11.2026	A1 & A2	A3 & A4	A5 & A6

BATCHES:-

A1→	1602-24-735-001 TO 004, 006 TO 1602-24-735-013;	12
A2→	1602-24-735-014 TO 1602-24-735-025;	12
A3→	1602-24-735-026 TO 1602-24-735-037;	12
A4→	1602-24-735-038 TO 1602-24-735-049;	12
A5→	1602-24-735-050 TO 1602-24-735-061;	12
A6→	1602-24-735-062 TO 064; 1602-24-735-301 TO 306; + ASRC	

(*) ASRC: Any Supplementary Registered Candidates

Mini Project-II

DATES	9.40 AM TO 12.40 PM	1.20 PM TO 4.20 PM
03.12.2026	A1 TO A13	A14 TO A26

BATCHES:-

A1→	1602-24-735-008, 043, 060;	A14→	1602-24-735-007, 052, 058;
A2→	1602-24-735-036, 047, 055;	A15→	1602-24-735-030, 040, 303;
A3→	1602-24-735-010, 051, 053;	A16→	1602-24-735-035, 041, 056;
A4→	1602-24-735-009, 042, 037;	A17→	1602-24-735-017, 049;
A5→	1602-24-735-054, 006, 014;	A18→	1602-24-735-022;
A6→	1602-24-735-015, 019, 024;	A19→	1602-24-735-306, 018;
A7→	1602-24-735-023;	A20→	1602-24-735-033, 050, 057;
A8→	1602-24-735-026;	A21→	1602-24-735-002, 032, 045;
A9→	1602-24-735-013, 059, 063;	A22→	1602-24-735-025, 027, 031;
A10→	1602-24-735-034, 048, 062;	A23→	1602-24-735-038, 039;
A11→	1602-24-735-020, 028, 029;	A24→	1602-24-735-003, 016, 302;
A12→	1602-24-735-064, 046, 061;	A25→	1602-24-735-004, 021, 301;
A13→	1602-24-735-012, 011, 044;	A26→	1602-24-735-304, 001, 305; + ASRC

(*) ASRC: Any Supplementary Registered Candidates

CHIEF SUPERINTENDENT

VASAVI COLLEGE OF ENGINEERING (AUTONOMOUS)
IBRAHIMBAGH: HYDERABAD-31

Date: 10-11-2026

Time-Table for B.E V Semester (Main/Suppl.) Practical Examinations
Academic year 2026-2027

ECE (SECTION-B)

Microprocessors & Microcontrollers Lab

DATES	9.40 AM TO 12.40 PM	1.20 PM TO 4.20 PM
02.12.2026	B1 TO B3	B4 TO B6

Integrated Circuits & Applications Lab

DATES	9.00 AM TO 12.00 NOON	12.00 NOON TO 3.00 PM	3.00 PM TO 6.00PM
03.12.2026	B1 & B2	B3 & B4	B5 & B6

Analog and Digital Communication Lab

DATES	9.00 AM TO 12.00 NOON	12.00 NOON TO 3.00 PM	3.00 PM TO 6.00PM
04.12.2026	B1 & B2	B3 & B4	B5 & B6

BATCHES:-

B1→	1602-24-735-065 TO 1602-24-735-076;	12
B2→	1602-24-735-077 TO 1602-24-735-088;	12
B3→	1602-24-735-089 TO 1602-24-735-100;	12
B4→	1602-24-735-101 TO 1602-24-735-112;	12
B5→	1602-24-735-113 TO 119, 121 TO 1602-24-735-125;	12
B6→	1602-24-735-126, 127; 1602-24-735-307 TO 1602-24-735-312; 1602-23-735-075; + ASRC	

(*) ASRC: Any Supplementary Registered Candidates

Mini Project-II

DATES	9.40 AM TO 12.40 PM	1.20 PM TO 4.20 PM
30.11.2026	B1 TO B13	B14 TO B24

BATCHES:-

B1→	1602-24-735-069, 079, 112;	B14→	1602-24-735-307, 308, 311;
B2→	1602-24-735-111, 114, 116;	B15→	1602-24-735-119, 109, 075;
B3→	1602-24-735-093, 104, 105;	B16→	1602-24-735-065, 108, 095;
B4→	1602-24-735-096, 113, 117;	B17→	1602-24-735-098, 125, 122;
B5→	1602-24-735-097;	B18→	1602-24-735-071, 072, 092;
B6→	1602-24-735-067, 075;	B19→	1602-24-735-089;
B7→	1602-24-735-077, 085, 091;	B20→	1602-24-735-073, 088, 115;
B8→	1602-24-735-103, 086, 118;	B21→	1602-24-735-084, 090, 087;
B9→	1602-24-735-121, 094, 123;	B22→	1602-24-735-106, 078, 080, 081;
B10→	1602-24-735-068, 102, 124;	B23→	1602-24-735-312, 310, 087;
B11→	1602-24-735-082, 074, 107;	B24→	1602-24-735-066, 309, 099; + ASRC
B12→	1602-24-735-076, 101, 100;		
B13→	1602-24-735-127, 070, 126;		

(*) ASRC: Any Supplementary Registered Candidates

CHIEF SUPERINTENDENT

VASAVI COLLEGE OF ENGINEERING (AUTONOMOUS)
IBRAHIMBAGH: HYDERABAD-31

Date: 10-11-2026

Time-Table for B.E V Semester (Main/Suppl.) Practical Examinations
Academic year 2026-2027

ECE (SECTION-C)

Microprocessors & Microcontrollers Lab

DATES	9.40 AM TO 12.40 PM	1.20 PM TO 4.20 PM
03.12.2026	C1 TO C3	C4 TO C6

Integrated Circuits & Applications Lab

DATES	9.00 AM TO 12.00 NOON	12.00 NOON TO 3.00 PM	3.00 PM TO 6.00PM
04.12.2026	C1 & C2	C3 & C4	C5 & C6

Analog and Digital Communication Lab

DATES	9.00 AM TO 12.00 NOON	12.00 NOON TO 3.00 PM	3.00 PM TO 6.00PM
01.12.2026	C1 & C2	C3 & C4	C5 & C6

BATCHES:-

C1→	1602-24-735-128 TO 130, 132 TO 140;	12
C2→	1602-24-735-141 TO 148, 150 TO 1602-24-735-153;	12
C3→	1602-24-735-154 TO 1602-24-735-165;	12
C4→	1602-24-735-166 TO 1602-24-735-178;	12
C5→	1602-24-735-179 TO 1602-24-735-189;	12
C6→	1602-24-735-190 TO 192; 1602-24-735-313 TO 319; + ASRC	

(*) ASRC: Any Supplementary Registered Candidates

Mini Project-II

DATES	9.40 AM TO 12.40 PM	1.20 PM TO 4.20 PM
07.12.2026	C1 & C15	C16 & C26

BATCHES:-

C1→	1602-24-735-189;	C16→	1602-24-735-156, 159, 173;
C2→	1602-24-735-130;	C17→	1602-24-735-174, 313, 163;
C3→	1602-24-735-135, 144;	C18→	1602-24-735-165, 191, 161;
C4→	1602-24-735-155, 157, 172;	C19→	1602-24-735-133, 141, 151;
C5→	1602-24-735-145, 162;	C20→	1602-24-735-142, 181, 192;
C6→	1602-24-735-128, 190;	C21→	1602-24-735-315, 316, 319;
C7→	1602-24-735-136, 138, 139;	C22→	1602-24-735-171, 188, 143;
C8→	1602-24-735-134, 150, 170;	C23→	1602-24-735-317, 318;
C9→	1602-24-735-153, 164, 168;	C24→	1602-24-735-154, 137, 160;
C10→	1602-24-735-132, 169, 179;	C25→	1602-24-735-148, 183, 314;
C11→	1602-24-735-147, 177, 182;	C26→	1602-24-735-129, 146, 175;
C12→	1602-24-735-167, 184, 186;		
C13→	1602-24-735-158, 185, 187;		
C14→	1602-24-735-176, 178, 180;		
C15→	1602-24-735-166, 152, 140;		

(*) ASRC: Any Supplementary Registered Candidates

CHIEF SUPERINTENDENT

VASAVI COLLEGE OF ENGINEERING (AUTONOMOUS)
IBRAHIMBAGH: HYDERABAD-31

Date: 10-11-2026

Time-Table for (B.E Honours) V-Semester Practical Examinations for the Academic year 2026-2027.

ECE (SECTION-A, B & C)

FPGA Based System Design Lab

DATES	9.40 AM TO 12.40 PM	1.20 PM TO 4.20 PM
05.12.2026	P1 TO P30	P31 TO P60

BATCHES:-

P1→	1602-24-735-002;	P31→	1602-24-735-073;
P2→	1602-24-735-004;	P32→	1602-24-735-075;
P3→	1602-24-735-008;	P33→	1602-24-735-076;
P4→	1602-24-735-009;	P34→	1602-24-735-077;
P5→	1602-24-735-011;	P35→	1602-24-735-085;
P6→	1602-24-735-012;	P36→	1602-24-735-086;
P7→	1602-24-735-015;	P37→	1602-24-735-088;
P8→	1602-24-735-016;	P38→	1602-24-735-089;
P9→	1602-24-735-019;	P39→	1602-24-735-094;
P10→	1602-24-735-020;	P40→	1602-24-735-096;
P11→	1602-24-735-024;	P41→	1602-24-735-100;
P12→	1602-24-735-028;	P42→	1602-24-735-101;
P13→	1602-24-735-029;	P43→	1602-24-735-106;
P14→	1602-24-735-032;	P44→	1602-24-735-113;
P15→	1602-24-735-042;	P45→	1602-24-735-115;
P16→	1602-24-735-043;	P46→	1602-24-735-116;
P17→	1602-24-735-045;	P47→	1602-24-735-118;
P18→	1602-24-735-046;	P48→	1602-24-735-121;
P19→	1602-24-735-047;	P49→	1602-24-735-122;
P20→	1602-24-735-050;	P50→	1602-24-735-123;
P21→	1602-24-735-057;	P51→	1602-24-735-310;
P22→	1602-24-735-058;	P52→	1602-24-735-311;
P23→	1602-24-735-059;	P53→	1602-24-735-148;
P24→	1602-24-735-062;	P54→	1602-24-735-163;
P25→	1602-24-735-063;	P55→	1602-24-735-172;
P26→	1602-24-735-064;	P56→	1602-24-735-176;
P27→	1602-24-735-302;	P57→	1602-24-735-185;
P28→	1602-24-735-303;	P58→	1602-24-735-313;
P29→	1602-24-735-304;	P59→	1602-24-735-315;
P30→	1602-24-735-068;	P60→	1602-24-735-316 + ASRC

(*) ASRC: Any Supplementary Registered Candidates

CHIEF SUPERINTENDENT

VASAVI COLLEGE OF ENGINEERING (AUTONOMOUS)
IBRAHIMBAGH: HYDERABAD-31.

Date: 10-11-2026

Time-Table for B.E. V Semester (Main/Suppl.) Practical Examinations
Academic year 2026-2027

MECH

Manufacturing Processes Lab

DATES	9.40 AM TO 12.40 PM	1.20 PM TO 4.20 PM
03.12.2026	A1	A2
04.12.2026	A3	A4

CAD/CAM Lab

DATES	9.40 AM TO 12.40 PM	1.20 PM TO 4.20 PM
03.12.2026	A3	A4
04.12.2026	A1	A2

Programming for Mechanical Engineers Lab

DATES	9.40 AM TO 12.40 PM	1.20 PM TO 4.20 PM
30.11.2026	A1 & A2	A3 & A4

BATCHES:-

A1→	1602-24-736-001 TO 006, 008 TO 010, 013 TO 015, 017 TO 020;	16
A2→	1602-24-736-021 TO 028, 030 TO 035, 037, 038;	16
A3→	1602-24-736-039 TO 046, 048, 050, 052, 054 TO 058;	16
A4→	1602-24-736-059; 1602-24-736-301 TO 1602-24-736-308; 1602-23-736-002, 053;+ ASRC	

Mini Project

DATES	9.40 AM TO 12.40 PM	1.20 PM TO 4.20 PM
01.12.2026	A1 & A11	A12 & A22

BATCHES:-

A1→	1602-24-736-041, 044, 002;	A12→	1602-24-736-050, 014, 018;
A2→	1602-24-736-059, 054, 002;	A13→	1602-24-736-046, 055, 048;
A3→	1602-24-736-015, 058, 019;	A14→	1602-24-736-024, 308, 025;
A4→	1602-24-736-301, 056, 302;	A15→	1602-24-736-005, 010, 003;
A5→	1602-24-736-305, 304, 306;	A16→	1602-24-736-030, 035, 026;
A6→	1602-24-736-303, 045, 032;	A17→	1602-24-736-053, 009, 043;
A7→	1602-24-736-023, 008, 017;	A18→	1602-24-736-052, 031;
A8→	1602-24-736-038, 034, 013;	A19→	1602-24-736-037, 057;
A9→	1602-24-736-307, 004, 040;	A20→	1602-24-736-006, 028;
A10→	1602-24-736-033, 001, 021;	A21→	1602-24-736-042;
A11→	1602-24-736-039, 022, 027;	A22→	1602-24-736-020 + ASRC

(*) ASRC: Any Supplementary Registered Candidates

CHIEF SUPERINTENDENT

VASAVI COLLEGE OF ENGINEERING (AUTONOMOUS)
IBRAHIMBAGH: HYDERABAD-31

Date: 10-11-2026

Time-Table for B.E. V Semester (Main/Suppl.) Practical Examinations
Academic year 2026-2027

I.T (SECTION-A)

Computer Networks Lab

DATES	9.40 AM TO 12.40 PM	1.20 PM TO 4.20 PM
30.11.2026	A1	A2

Artificial Intelligence and Machine Learning Lab

DATES	9.40 AM TO 12.40 PM	1.20 PM TO 4.20 PM
03.12.2026	A1	A2

Software Engineering Lab

DATES	9.40 AM TO 12.40 PM	1.20 PM TO 4.20 PM
01.12.2026	A1	A2

Operating Systems Lab

DATES	9.40 AM TO 12.40 PM	1.20 PM TO 4.20 PM
04.12.2026	A1	A2

BATCHES:-

A1→	1602-24-737-001 TO 1602-24-737-034;	34
A2→	1602-24-737-037 TO 052, 054 TO 063; 1602-24-737-301 TO 307; +ASRC	

(*) ASRC: Any Supplementary Registered Candidates

Theme Based Project

DATES	9.40 AM TO 12.40 PM	1.20 PM TO 4.20 PM
02.12.2026	A1 TO A12	A13 TO A23

BATCHES:-

A1→	1602-24-737-048, 017, 021;	A13→	1602-24-737-022, 019, 060;
A2→	1602-24-737-055, 010, 043;	A14→	1602-24-737-002, 001, 012;
A3→	1602-24-737-056, 057;	A15→	1602-24-737-023, 016, 014;
A4→	1602-24-737-020, 032, 306;	A16→	1602-24-737-302, 028, 018;
A5→	1602-24-737-041, 050, 059;	A17→	1602-24-737-049, 303, 033;
A6→	1602-24-737-004, 006, 025;	A18→	1602-24-737-024, 304, 042;
A7→	1602-24-737-062, 063, 029;	A19→	1602-24-737-008, 009, 031;
A8→	1602-24-737-061, 045, 034;	A20→	1602-24-737-054, 305, 307;
A9→	1602-24-737-026, 037, 046;	A21→	1602-24-737-040, 047, 301;
A10→	1602-24-737-051, 044, 052;	A22→	1602-24-737-005, 030, 039;
A11→	1602-24-737-027, 003, 007;	A23→	1602-24-737-058, 038; + ASRC
A12→	1602-24-737-013, 015, 011;		

CHIEF SUPERINTENDENT

VASAVI COLLEGE OF ENGINEERING (AUTONOMOUS)
IBRAHIMBAGH: HYDERABAD-31

Date: 10-11-2026

Time-Table for B.E. V Semester (Main/Suppl.) Practical Examinations
Academic year 2026-2027

I.T (SECTION-B)

Computer Networks Lab

DATES	9.40 AM TO 12.40 PM	1.20 PM TO 4.20 PM
01.12.2026	B1	B2

Artificial Intelligence and Machine Learning Lab

DATES	9.40 AM TO 12.40 PM	1.20 PM TO 4.20 PM
02.12.2026	B1	B2

Software Engineering Lab

DATES	9.40 AM TO 12.40 PM	1.20 PM TO 4.20 PM
30.11.2026	B1	B2

Operating Systems Lab

DATES	9.40 AM TO 12.40 PM	1.20 PM TO 4.20 PM
03.12.2026	B1	B2

BATCHES:-

B1→	1602-24-737-065 TO 068, 070 TO 080, 082 TO 088, 090 TO 100;	37
B2→	1602-24-737-101 TO 103, 105, 106, 108 TO 110, 112 TO 128; 1602-24-737-308 TO 1602-24-737-314; + ASRC	

(*) ASRC: Any Supplementary Registered Candidates

Theme Based Project

DATES	9.40 AM TO 12.40 PM	1.20 PM TO 4.20 PM
04.12.2026	B1 TO B11	B12 TO B22

BATCHES:-

B1→	1602-24-737-066, 073, 084;	B12→	1602-24-737-116, 120, 115;
B2→	1602-24-737-086, 091, 095;	B13→	1602-24-737-070, 071, 079;
B3→	1602-24-737-109, 106, 121;	B14→	1602-24-737-099, 101, 113;
B4→	1602-24-737-093, 119, 078;	B15→	1602-24-737-087, 105, 126;
B5→	1602-24-737-100, 313, 312;	B16→	1602-24-737-098, 090, 110;
B6→	1602-24-737-077, 085, 097;	B17→	1602-24-737-072, 096, 103;
B7→	1602-24-737-092, 076, 124;	B18→	1602-24-737-080, 108, 112;
B8→	1602-24-737-075, 308, 074;	B19→	1602-24-737-068, 083, 094;
B9→	1602-24-737-067, 088, 127;	B20→	1602-24-737-114, 082, 102;
B10→	1602-24-737-122, 128, 314;	B21→	1602-24-737-065, 123;
B11→	1602-24-737-310, 125, 117;	B22→	1602-24-737-118, 311, 309;

CHIEF SUPERINTENDENT

VASAVI COLLEGE OF ENGINEERING (AUTONOMOUS)
IBRAHIMBAGH: HYDERABAD-31

Date: 10-11-2026

Time-Table for B.E. V Semester (Main/Suppl.) Practical Examinations
Academic year 2026-2027

I.T (SECTION-C)

Computer Networks Lab

DATES	9.40 AM TO 12.40 PM	1.20 PM TO 4.20 PM
01.12.2026	C1	C2

Artificial Intelligence and Machine Learning Lab

DATES	9.40 AM TO 12.40 PM	1.20 PM TO 4.20 PM
04.12.2026	C1	C2

Software Engineering Lab

DATES	9.40 AM TO 12.40 PM	1.20 PM TO 4.20 PM
02.12.2026	C1	C2

Operating Systems Lab

DATES	9.40 AM TO 12.40 PM	1.20 PM TO 4.20 PM
03.12.2026	C1	C2

BATCHES:-

C1→	1602-24-737-129 TO 134, 136 TO 1602-24-737-165;	36
C2→	1602-24-737-166 TO 184, 186 TO 193; 1602-24-737-315 TO 321; 1602-23-737-133, 188; +ASRC	

(*) ASRC: Any Supplementary Registered Candidates

Theme Based Project

DATES	9.40 AM TO 12.40 PM	1.20 PM TO 4.20 PM
30.11.2026	C1 TO C12	C13 TO C24

BATCHES:-

C1→	1602-24-737-140, 167, 177;	C13→	1602-24-737-134, 172, 174;
C2→	1602-24-737-181, 182, 189;	C14→	1602-24-737-153, 158, 162;
C3→	1602-24-737-141, 183, 186;	C15→	1602-24-737-149, 159, 133;
C4→	1602-24-737-146, 151, 164;	C16→	1602-24-737-148, 173, 175;
C5→	1602-24-737-163, 192, 321;	C17→	1602-24-737-136, 152, 154;
C6→	1602-24-737-171, 180, 193;	C18→	1602-24-737-142, 143, 156;
C7→	1602-24-737-190, 191, 318;	C19→	1602-24-737-155, 316, 320;
C8→	1602-24-737-188, 317, 319;	C20→	1602-24-737-166, 168, 184;
C9→	1602-24-737-165, 170, 179;	C21→	1602-24-737-144, 145, 187;
C10→	1602-24-737-132, 147, 150;	C22→	1602-24-737-160, 161, 169;
C11→	1602-24-737-130, 176, 178;	C23→	1602-24-737-129, 131, 315;
C12→	1602-24-737-133, 138, 139;	C24→	1602-24-737-137, 157, 188; + ASRC

CHIEF SUPERINTENDENT

VASAVI COLLEGE OF ENGINEERING (AUTONOMOUS)
IBRAHIMBAGH: HYDERABAD-31.

Date: 10-11-2026

Time-Table for (B.E. Honours in AI&ML) V-Semester (Main/Suppl.)
Practical Examinations for the Academic year 2026-2027.

I.T (SECTION-A, B & C)

Computational Intelligence Lab

DATES	9.40 AM TO 12.40 PM	1.20 PM TO 4.20 PM
05.12.2026	P1	P2

BATCHES:-

P1→	1602-24-737-001, 006, 013, 016, 017, 020, 024, 026, 030, 045, 047, 048, 049, 051, 056, 57, 061, 062, 0301, 303, 304, 306, 065, 066, 070, 077, 078, 085, 086, 090, 092, 093, 095, 098, 100, 103;	36
P2→	1602-24-737-105, 109, 112, 113, 115, 116, 118, 121, 123, 126, 128, 310, 132, 133, 134, 136, 138, 145, 147, 148, 149, 152, 155, 164, 170, 174, 177, 179, 184, 186, 187, 188, 189, 192, 316; +ASRC	

(*) ASRC: Any Supplementary Registered Candidates

CHIEF SUPERINTENDENT