



VASAVI COLLEGE OF ENGINEERING (A): HYDERABAD
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Program Exit Survey and Action Taken Report

Academic Year 2023-24

S.No.	Suggestion on	Student Name	Suggestion/Feed back	Action taken
1	Curriculum	Anjana Shroff (1602-20-735-066)	More practical aspects to be included.	<ul style="list-style-type: none">• New Hardware based experiments were introduced in the following lab courses<ul style="list-style-type: none">– Interfacing experiments in Microcontrollers lab– SDR based experiments introduced in ADC lab.
		Charan Reddy. M (1602-20-735-070)	Encourage open book exams.	<ul style="list-style-type: none">• Open book exams on the following courses were conducted.<ul style="list-style-type: none">– Digital Logic Design– Electronic Circuits– Digital Signal Processing– Microprocessors & Microcontrollers
2	Teaching learning process	Ayesha (1602-20-735-003)	Level of assignments for good practice.	<ul style="list-style-type: none">• Different sets of assignments at higher levels of Blooms taxonomy were given to the students in different courses.
		Keerthana. Ch (1602-20-735-076)	Hands on experience on Hardware and advanced technologies.	<ul style="list-style-type: none">• Hands on experiments were introduced in the following hardware based elective lab courses.<ul style="list-style-type: none">– Advanced Embedded Systems Lab– IoT Architectures and Applications Lab– SDR and its Application Lab
3	Infrastructure facilities	Anish. W (1602-20-735-066)	Improve Sports Infrastructure.	<ul style="list-style-type: none">• Sports infrastructure like volley ball and basketball were renovated.• New sports infrastructure like football and cricket net practice were added to the existing facilities.
		Abhinay Sreeram. N (1602-20-735-121)	Quality of Canteen food can be improved.	<ul style="list-style-type: none">• Feedback was shared with the Canteen Committee to improve the food quality.

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4	Industrial visit	Manoj. Ch (1602-20-735-082)	Industrial visits should happen once in a year, so that individual may know what's happening outside.	<ul style="list-style-type: none">• Following Industrial visits have been arranged.<ul style="list-style-type: none">- HBL Power Systems on 09.11.2023.- NRSC, Jeedimetla on 26.03.2024- NRSC, Shadnagar on 16.11.2023- Astra Microwaves on 10.10.2023- Medha Servo Drives on 20.9.2024- Lotus Antennas and Microwave and Technologies on 19.9.2024

Dr

S. Sreenivasa Rao

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Parent Survey and Action Taken Report

Academic Year 2023-24

Parent Name	Suggestion/Feed back	Action taken
D. Srinivasa Rao F/o D. Anvesh (1602-22-735-005)	Parent requested to increase the Sports facility and Sports hours.	<ul style="list-style-type: none">• Sports facilities like volley ball and basketball were renovated.• New sports facilities like football and cricket net practice were added to the existing facilities.
B. Surendranath M/o B. Siriharshitha (1602-21-735-313)	Encourage the students to participate in Sports	<ul style="list-style-type: none">• To encourage students, several sports events like "Vasavi Premier League" Sports Meet was Organized with tracks like Cricket, Throw Ball, Basket Ball, Volley Ball and Table Tennis during 2024-25.• Our students have also participated at national and international sports events organized by other institutions. Details are given below<ul style="list-style-type: none">– National level sports in inter engineering college sports fest conducted by CBIT.– International Chess competition organised by OU during 8-9 November, 2024– Ekalavya Inter Engineering college sports meet by MGIT, in November-2024

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Employer Survey and Action Taken Report

Academic Year 2023-24

S.No.	Employer Details	Suggestion/Feed back	Action taken
1	D.V.R. Murthy, MosChip Technologies Limited, Hyderabad	Students must be strong in fundamentals and suggested students to solve the problems of standard text books	<ul style="list-style-type: none">• Faculty offering courses in Electronic Circuits, Microprocessors and Microcontrollers, Digital Logic Design and Digital Signal Processing courses were giving different sets of assignments and problems solving questions from standard text books.• Open book exams were also conducted in the above courses.
2	Lakshmi Deepika K TA Team Leader, Verisk, Hyderabad	Students can improve more abilities in designing a system to meet the company needs	<ul style="list-style-type: none">• To bring awareness on system design and familiarize with the current trends and technologies of the industry, following industry visits were arranged for the 3rd year students in the A.Y 2024-25.<ul style="list-style-type: none">– Lotus Microwaves, Hyderabad.– Medha Servo Drives, Hyderabad– Astra Microwaves, Hyderabad– NRSC, Hyderabad– HBL, Hyderabad
3	Prudhviraj Member-Technical staff Valuka Solutions, Hyderabad	Students should focus on honing their skills in digital design and Verilog, dedicating ample time to practice Verilog coding exercises	<ul style="list-style-type: none">• Assignments and Mini projects were given to the students covering Verilog coding topics in Digital Logic Design and FPGA courses.
4	Vikram Pujari University hiring Lead, NSL Hub, Hyderabad	Effective communication in both verbal and written forms can be improved and also make aware of contemporary issues	<ul style="list-style-type: none">• To improve effective communication skills in both verbal and written the following Skill Development courses have been offered.<ul style="list-style-type: none">– Quantitative Aptitude– Reasoning Aptitude– Verbal Aptitude– Communication Skills in English
5	Mr. M.S.S. Prasad Rao, Software Engineer, TCS, Hyderabad	The standard of students quality is meeting our business requirements.	-

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BOS ACTION TAKEN REPORT

Minutes of the Board of Studies meeting held on 24.06.2023 for Academic Year 2023-24

S. No	Course Name	BOS Comment / Action taken	Expert who suggested change	PO/PSO Mapping
1	Language models on applications	Comment: Dr. D. Ranganadham suggested to include AI and ML based courses like language models to the students. Action taken: Based on Dr. Ranganadham suggestion, our faculty Mr. B. Uma Mahesh Babu and Dr. K. Veera Swamy proposed to introduce new professional elective course on "Language Models on Applications" in signal processing elective stream.	Dr. D. Ranganadham, Deputy Director General, Doordarshan Kendra, Vijayawada, Industry Representative	PO1, PO2, PO5, PSO3
2	Mini Project / Theme based Project / Course Project	Comment: Sri. J Sunil suggested to provide online cadence and other EDA tools access to the students and faculty through Virtual Private Network facility (VPN). Action taken: VPN facility was established for students and faculty to access EDA tools extensively for solving the assignments, Mini Projects & Major Projects	Sri.J.Sunil, CEO, Valuka Solutions Pvt Ltd, Hyderabad	PO1, PO2, PO3, PO4, PO5, PO6, PO7, PO8, PO9, PO10, PO11, PO12
3	FPGA based System Design	Comment: Sri. Anand Moghe suggested to include few topics in "FPGA based System Design" course in Honours program. Action taken: FPGA performance parameters like Maximum frequency calculation, set up time, Clock skew, Jitter and Propagation Delay topics were included in "FPGA based System Design" course.	Sri. Anand Moghe, Former Vice President, Qualcomm Logic Ltd, Special Invitee	PO1, PO2, PO5, PSO1
4	Design Verification	Comment: Sri. J. Sunil suggested to involve Industry persons in course projects. Action taken: Based on the request of Chairman, BoS, Sri J. Sunil offered 10 problem statements on Design verification related topics to the Honours students during VII-semester.		PO1, PO2, PO3, PO4, PO5, PO12, PSO1, PSO2
5	Programming techniques for problem solving	Comment: Sri. Thati Rahul suggested to include one unit in "Programming Techniques for Problem Solving" course in III-Semester for lateral entry students, as these topics are very useful in VLSI verification related courses. Action taken: one unit covering OOPs concepts in "Programming Techniques for Problem Solving" Course are introduced for lateral entry students.	Sri. Thati Rahul, Alumnus, Senior Systems Design Engineer, Xilinx India Technologies Pvt. Ltd., Hyderabad	PO1, PO2, PO3, PO12, PSO1, PSO2, PSO3


Faculty In-charge


Chairman, BoS - ECE



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Alumni Feedback and Action Taken Report

Academic Year 2023-24

Suggestions given by Alumni	Alumni Details	Action Taken
Insist the students to learn basic concepts in depth on Basic Electronic Circuits.	Bethi Srinivas, CEO, Made Easy Educational PVT LTD, New Delhi, 1986	<ul style="list-style-type: none">• Faculty offering Basic Electronic Circuits are giving different sets of assignments covering basic concepts, Design, Analysis and Application related problems.• Alumni Guest Lectures were also arranged on the concepts like Negative feedback in Electronic Circuits.
Skills to be improved in IoT Technologies as per industry needs.	M. Praveen Kumar, Application Engineer, Mphasis, Hyderabad, 2011	<ul style="list-style-type: none">• ECE Department introduced new professional elective lab course on "IoT Architectures and Applications" with industry standard latest hardware boards in VII semester to enhance the skills.• Alumni Technical Talk on "IoT and Future Opportunities" was also conducted for IV semester students.
Focus on Career Plans	G. Niranjan Kumar, Engineer, Valuka Solutions Pvt Ltd, 2022	Career guidance sessions are being conducted in association with Y-axis careers on higher education and CAT training by Mr. Sarath Chander G and awareness on GATE preparation by ACE Engineering Academy.

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